

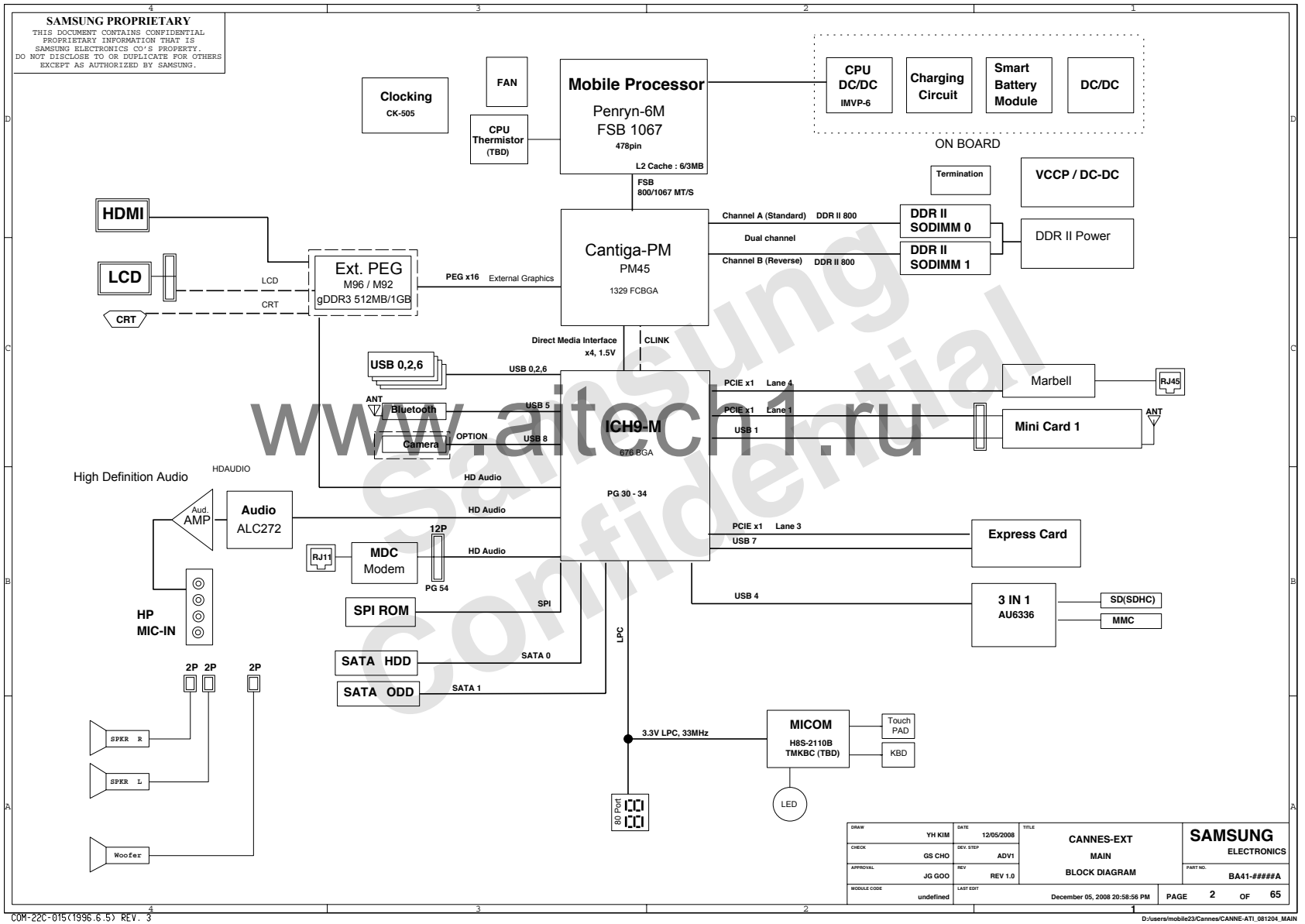
8. Block Diagram and Schematic

| SAMSUNG PROPRIETARY THIS DOCUMENT CONTAINS CONFIDENTIAL PROPRIETARY INFORMATION THAT IS SAMSUNG ELECTRONICS CO.'S PROPERTY. DO NOT DISCLOSE TO OR DUPLICATE FOR OTHERS EXCEPT AS AUTHORIZED BY SAMSUNG. | | | | | | | | | | | | | | | |
|--|----------------|--|--|------------|--------------|----------|--------|----------|----------------|-----------|------|----------|-------|-----------|--------------|
| <h1>CANNES-EXT(GFX:AMD)</h1> | | | | | | | | | | | | | | | |
| CPU : Intel Penryn Chip Set : PM45 & ICH9M Graphic : M96/M92/gDDR3 1G Remarks : Montevina Platform | | | | | | | | | | | | | | | |
| <table border="1"> <tr> <td>Model Name</td> <td>: CANNES-EXT</td> </tr> <tr> <td>PBA Name</td> <td>: MAIN</td> </tr> <tr> <td>PCB Code</td> <td>: BA41-xxxxxxA</td> </tr> <tr> <td>Dev. Step</td> <td>: MP</td> </tr> <tr> <td>Revision</td> <td>: 1.0</td> </tr> <tr> <td>T.R. Date</td> <td>: 2009.04.21</td> </tr> </table> | | | | Model Name | : CANNES-EXT | PBA Name | : MAIN | PCB Code | : BA41-xxxxxxA | Dev. Step | : MP | Revision | : 1.0 | T.R. Date | : 2009.04.21 |
| Model Name | : CANNES-EXT | | | | | | | | | | | | | | |
| PBA Name | : MAIN | | | | | | | | | | | | | | |
| PCB Code | : BA41-xxxxxxA | | | | | | | | | | | | | | |
| Dev. Step | : MP | | | | | | | | | | | | | | |
| Revision | : 1.0 | | | | | | | | | | | | | | |
| T.R. Date | : 2009.04.21 | | | | | | | | | | | | | | |
| <table border="1"> <thead> <tr> <th>Design</th> <th>CHECK</th> <th>APPROVAL</th> </tr> </thead> <tbody> <tr> <td> </td> <td> </td> <td> </td> </tr> <tr> <td> </td> <td> </td> <td> </td> </tr> </tbody> </table> | | | | Design | CHECK | APPROVAL | | | | | | | | | |
| Design | CHECK | APPROVAL | | | | | | | | | | | | | |
| | | | | | | | | | | | | | | | |
| | | | | | | | | | | | | | | | |
| Owner : SEC Mobile R & D | | Signature : X | | | | | | | | | | | | | |
| COM-22C-015(1996.6.5) REV. 3 | | December 05, 2008 20:58:56 PM | | | | | | | | | | | | | |
| WK.YEO | | DATE 04/21/2009 | | | | | | | | | | | | | |
| SH.LEE | | REV STEP MP | | | | | | | | | | | | | |
| KK.BIN | | REV REV 1.0 | | | | | | | | | | | | | |
| MODULE CODE | | LAST EDIT | | | | | | | | | | | | | |
| SAMSUNG ELECTRONICS | | PART NO. BA41-####A | | | | | | | | | | | | | |
| CANNES-EXT MAIN COVER | | December 05, 2008 20:58:56 PM | | | | | | | | | | | | | |
| PAGE 1 OF 65 | | D:\users\mobile23\Cannes\CANNE-ATI_081204_MAIN | | | | | | | | | | | | | |

www.rosefix.com

www.aitech1.ru

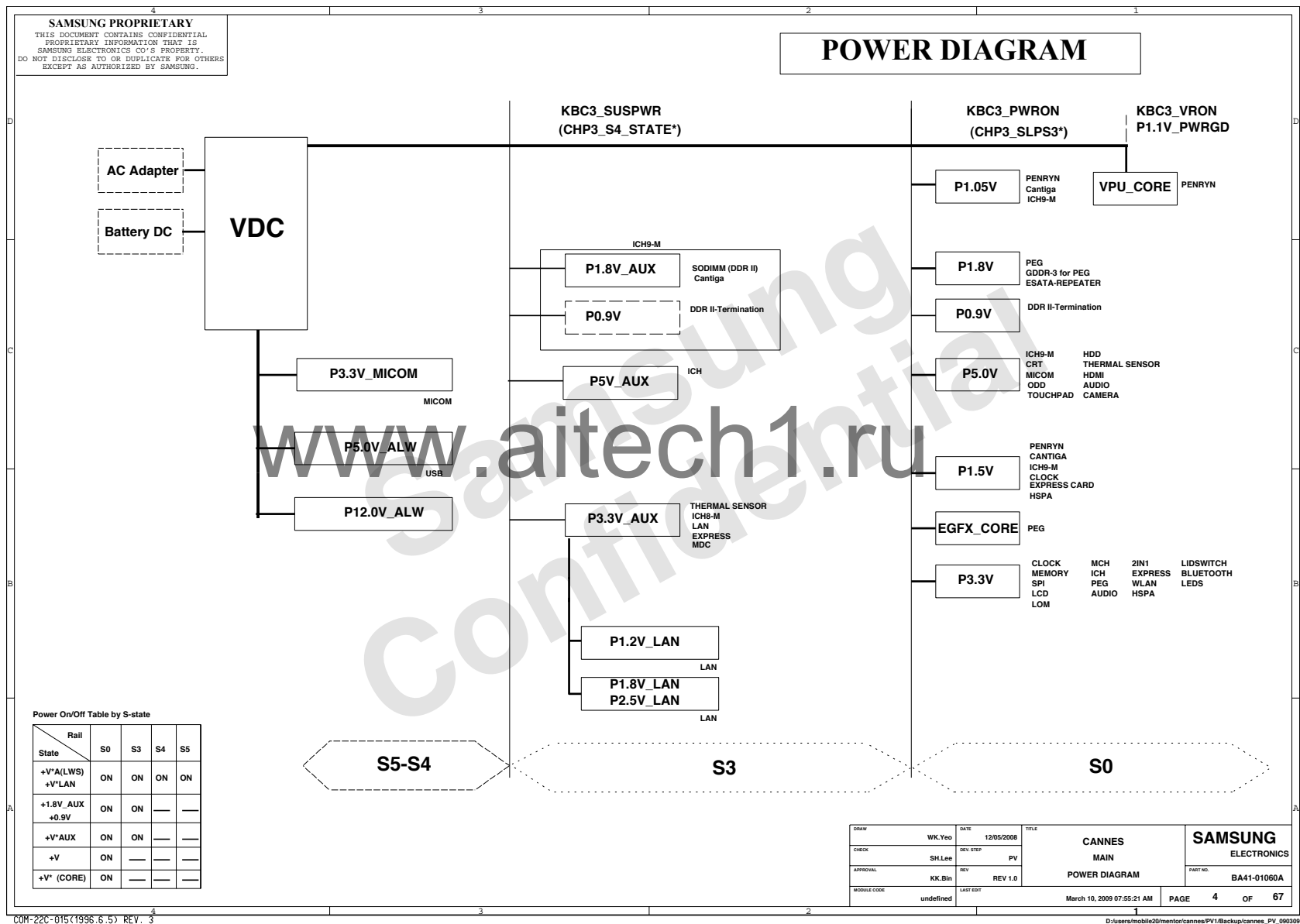
8. Block Diagram and Schematic



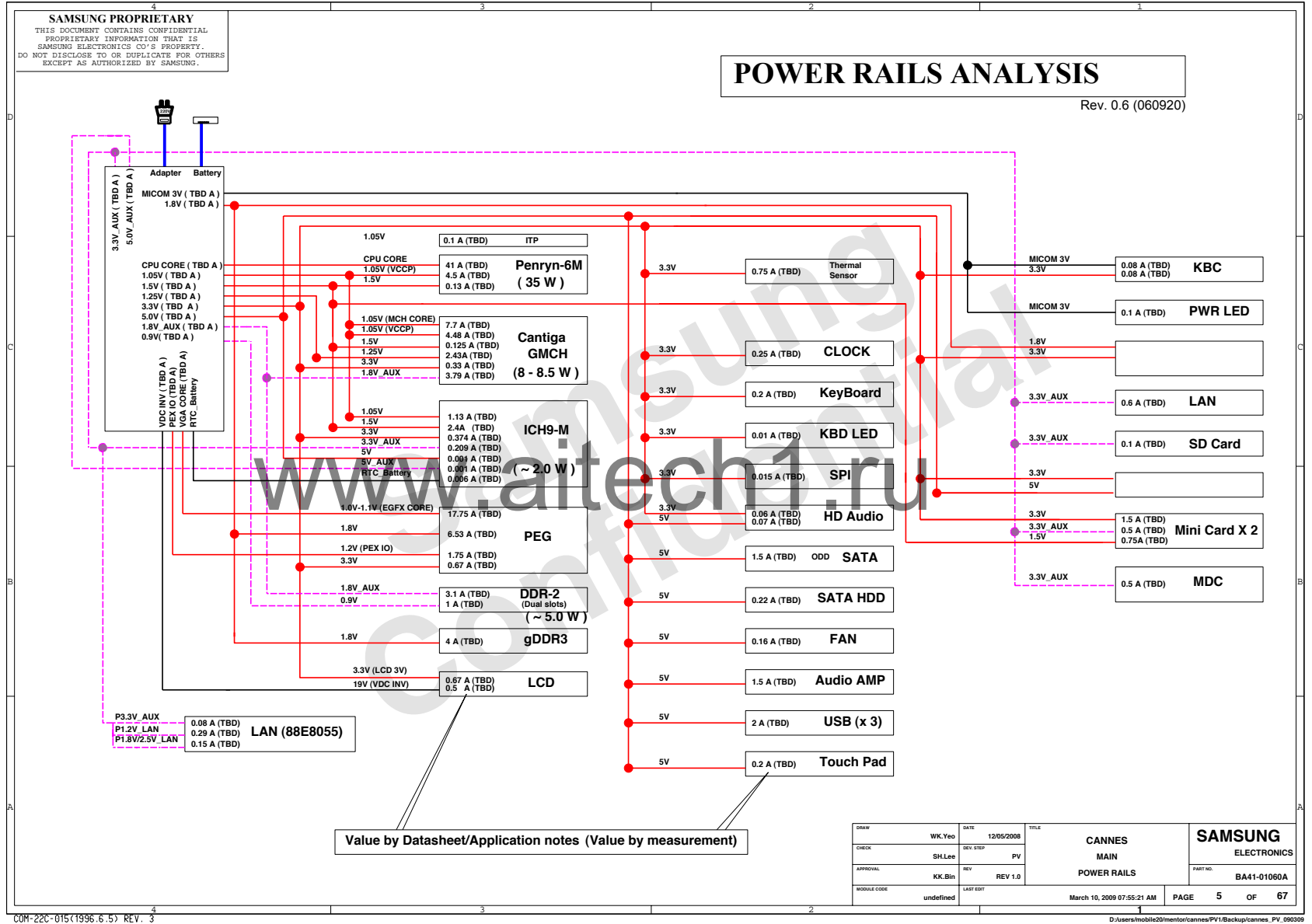
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|---|--|---|--|--|------------------------------|-------------------------|----------------|---------------------------------|-------------|-------------------------------------|------------------------|--|---|---|-----------|--|---------------------------------|--|-------------|---|-----------|--|-----------|--|-----------|--|-----------|-------------------------|---------|-----------|--------|---|----------------------------|--------------|--------------------|----------------|---------|----------------|-----------------|-----------|---------|-------|---------|-----------|-----|---|---------------------------|-----------|-----|---|---------------------------|-----------|-----|---|---------------------------|-----------|-----|------------------------------------|--|--|--|
| SCHEMATIC ANNOTATIONS AND BOARD INFORMATION | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| <div>PCI Devices</div> <table> <tr> <th>Devices</th><th>IDSEL#</th><th>REQ/IGNT#</th><th>Interrupts</th></tr> <tr> <td>Cardbus</td><td>AD25</td><td>3</td><td>A,B,C</td></tr> <tr> <td>USB</td><td>AD29(internal)</td><td>-</td><td>USB2.0 #0 (USB0) : A USB2.0 #1 (USB1) : D USB2.0 #2 (USB4) : C USB2.0 #3 (USB5) : E USB2.0 #4 (EHCI) : H</td></tr> <tr> <td>Hub to PCI LPC bridge/IDE/AC97/SMBUS</td><td>AD30(internal) AD31(internal)</td><td>- -</td><td>- B</td></tr> <tr> <td>Internal MAC AC Link GLAN</td><td>AD24(internal) - -</td><td>- - -</td><td>E B F</td></tr> </table> | Devices | IDSEL# | REQ/IGNT# | Interrupts | Cardbus | AD25 | 3 | A,B,C | USB | AD29(internal) | - | USB2.0 #0 (USB0) : A USB2.0 #1 (USB1) : D USB2.0 #2 (USB4) : C USB2.0 #3 (USB5) : E USB2.0 #4 (EHCI) : H | Hub to PCI LPC bridge/IDE/AC97/SMBUS | AD30(internal) AD31(internal) | - - | - B | Internal MAC AC Link GLAN | AD24(internal) - - | - - - | E B F | | | | <div>Crystal / Oscillator</div> <table> <tr> <th>TYPE</th><th>FREQUENCY</th><th>DEVICE</th><th>USAGE</th></tr> <tr> <td>Crystal</td><td>32.768KHz</td><td>ICH8-M</td><td>Real Time Clock</td></tr> <tr> <td>Crystal</td><td>10MHz</td><td>MICOM</td><td>HD64F2169/2160</td></tr> <tr> <td>Crystal</td><td>14.318MHz</td><td>CLOCK-Generator</td><td>CK-505</td></tr> <tr> <td>Crystal</td><td>25MHz</td><td>LAN</td><td>Intel LAN</td></tr> </table> | TYPE | FREQUENCY | DEVICE | USAGE | Crystal | 32.768KHz | ICH8-M | Real Time Clock | Crystal | 10MHz | MICOM | HD64F2169/2160 | Crystal | 14.318MHz | CLOCK-Generator | CK-505 | Crystal | 25MHz | LAN | Intel LAN | | | | | | | | | | | | | | | | | |
| Devices | IDSEL# | REQ/IGNT# | Interrupts | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| Cardbus | AD25 | 3 | A,B,C | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| USB | AD29(internal) | - | USB2.0 #0 (USB0) : A USB2.0 #1 (USB1) : D USB2.0 #2 (USB4) : C USB2.0 #3 (USB5) : E USB2.0 #4 (EHCI) : H | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| Hub to PCI LPC bridge/IDE/AC97/SMBUS | AD30(internal) AD31(internal) | - - | - B | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| Internal MAC AC Link GLAN | AD24(internal) - - | - - - | E B F | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| TYPE | FREQUENCY | DEVICE | USAGE | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| Crystal | 32.768KHz | ICH8-M | Real Time Clock | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| Crystal | 10MHz | MICOM | HD64F2169/2160 | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| Crystal | 14.318MHz | CLOCK-Generator | CK-505 | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| Crystal | 25MHz | LAN | Intel LAN | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| <div>Voltage Rails</div> <table> <tr> <td>VDC</td><td>Primary DC system power supply (7 to 21V)</td></tr> <tr> <td>VCC_CORE</td><td>Core Voltage for CPU</td></tr> <tr> <td>GFX_CORE</td><td>Core Voltage for GPU</td></tr> <tr> <td>P1.05V (VCCOP)</td><td>VTT for CPU, Crestline & ICH8-M</td></tr> <tr> <td>P3.3V_MICOM</td><td>3.3V always power rail (for Microm)</td></tr> <tr> <td>P1.5V</td><td>1.5V switched power rail (off in S3-S5)</td></tr> <tr> <td>P1.8V</td><td>1.8V switched power rail (off in S3-S5)</td></tr> <tr> <td>P1.8V_AUX</td><td>1.8V power rail for DDR (off in S4-S5)</td></tr> <tr> <td>P0.9V</td><td>0.9V power rail for DDR (off in S3-S5)</td></tr> <tr> <td>P3.3V</td><td>3.3V switched power rail (off in S3-S5)</td></tr> <tr> <td>P3.3V_AUX</td><td>3.3V switched on power rail (off in S4-S5)</td></tr> <tr> <td>P5.0V</td><td>5.0V switched power rail (off in S3-S5)</td></tr> <tr> <td>P5.0V_AUX</td><td>5.0V switched on power rail (off in S4-S5)</td></tr> <tr> <td>P5.0V_ALW</td><td>5.0V always power rail</td></tr> </table> | VDC | Primary DC system power supply (7 to 21V) | VCC_CORE | Core Voltage for CPU | GFX_CORE | Core Voltage for GPU | P1.05V (VCCOP) | VTT for CPU, Crestline & ICH8-M | P3.3V_MICOM | 3.3V always power rail (for Microm) | P1.5V | 1.5V switched power rail (off in S3-S5) | P1.8V | 1.8V switched power rail (off in S3-S5) | P1.8V_AUX | 1.8V power rail for DDR (off in S4-S5) | P0.9V | 0.9V power rail for DDR (off in S3-S5) | P3.3V | 3.3V switched power rail (off in S3-S5) | P3.3V_AUX | 3.3V switched on power rail (off in S4-S5) | P5.0V | 5.0V switched power rail (off in S3-S5) | P5.0V_AUX | 5.0V switched on power rail (off in S4-S5) | P5.0V_ALW | 5.0V always power rail | | | | <div>LCD Pannel Detect (TBD)</div> <table> <tr> <th>Devices</th><th>Resolution</th><th>PANNEL_DETECT_0</th></tr> <tr> <td></td><td></td><td></td></tr> </table> | Devices | Resolution | PANNEL_DETECT_0 | | | | | | | | | | | | | | | | | | | | | | | | | | |
| VDC | Primary DC system power supply (7 to 21V) | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| VCC_CORE | Core Voltage for CPU | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| GFX_CORE | Core Voltage for GPU | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| P1.05V (VCCOP) | VTT for CPU, Crestline & ICH8-M | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| P3.3V_MICOM | 3.3V always power rail (for Microm) | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| P1.5V | 1.5V switched power rail (off in S3-S5) | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| P1.8V | 1.8V switched power rail (off in S3-S5) | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| P1.8V_AUX | 1.8V power rail for DDR (off in S4-S5) | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| P0.9V | 0.9V power rail for DDR (off in S3-S5) | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| P3.3V | 3.3V switched power rail (off in S3-S5) | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| P3.3V_AUX | 3.3V switched on power rail (off in S4-S5) | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| P5.0V | 5.0V switched power rail (off in S3-S5) | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| P5.0V_AUX | 5.0V switched on power rail (off in S4-S5) | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| P5.0V_ALW | 5.0V always power rail | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| Devices | Resolution | PANNEL_DETECT_0 | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| <div>USB PORT Assign</div> <table> <tr> <th>PORT #</th><th>ASSIGNED TO</th></tr> <tr> <td>0</td><td>SYSTEM PORT 0</td></tr> <tr> <td>1</td><td>SYSTEM PORT 1</td></tr> <tr> <td>2</td><td>SYSTEM PORT 2</td></tr> <tr> <td>3</td><td>NC</td></tr> <tr> <td>4</td><td>NC</td></tr> <tr> <td>5</td><td>Bluetooth</td></tr> <tr> <td>6</td><td>Mini PCI Express 2</td></tr> <tr> <td>7</td><td>Camera</td></tr> <tr> <td>8</td><td>NC</td></tr> <tr> <td>9</td><td>NC</td></tr> </table> | PORT # | ASSIGNED TO | 0 | SYSTEM PORT 0 | 1 | SYSTEM PORT 1 | 2 | SYSTEM PORT 2 | 3 | NC | 4 | NC | 5 | Bluetooth | 6 | Mini PCI Express 2 | 7 | Camera | 8 | NC | 9 | NC | | | | <div>I²C / SMB Address</div> <table> <tr> <th>Devices</th><th>Address</th><th>Hex</th><th>Bus</th></tr> <tr> <td>ICH8-m</td><td>Master</td><td>-</td><td>SMBUS Master</td></tr> <tr> <td>CPU Thermal Sensor</td><td>0111 101x</td><td>7Ah</td><td>Thermal Sensor</td></tr> <tr> <td>SODIMM0</td><td>1010 000x</td><td>A0h</td><td>-</td></tr> <tr> <td>SODIMM1</td><td>1010 010x</td><td>A4h</td><td>-</td></tr> <tr> <td>Thermal Sensor on SODIMM0</td><td>0011 000x</td><td>3Ch</td><td>-</td></tr> <tr> <td>Thermal Sensor on SODIMM1</td><td>0011 010x</td><td>34h</td><td>-</td></tr> <tr> <td>CK-505M (Clock Generator)</td><td>110T 001x</td><td>B2h</td><td>Clock, Unused Clock Output Disable</td></tr> </table> | Devices | Address | Hex | Bus | ICH8-m | Master | - | SMBUS Master | CPU Thermal Sensor | 0111 101x | 7Ah | Thermal Sensor | SODIMM0 | 1010 000x | A0h | - | SODIMM1 | 1010 010x | A4h | - | Thermal Sensor on SODIMM0 | 0011 000x | 3Ch | - | Thermal Sensor on SODIMM1 | 0011 010x | 34h | - | CK-505M (Clock Generator) | 110T 001x | B2h | Clock, Unused Clock Output Disable | | | |
| PORT # | ASSIGNED TO | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 0 | SYSTEM PORT 0 | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 1 | SYSTEM PORT 1 | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 2 | SYSTEM PORT 2 | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 3 | NC | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 4 | NC | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 5 | Bluetooth | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 6 | Mini PCI Express 2 | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 7 | Camera | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 8 | NC | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 9 | NC | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| Devices | Address | Hex | Bus | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| ICH8-m | Master | - | SMBUS Master | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| CPU Thermal Sensor | 0111 101x | 7Ah | Thermal Sensor | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| SODIMM0 | 1010 000x | A0h | - | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| SODIMM1 | 1010 010x | A4h | - | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| Thermal Sensor on SODIMM0 | 0011 000x | 3Ch | - | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| Thermal Sensor on SODIMM1 | 0011 010x | 34h | - | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| CK-505M (Clock Generator) | 110T 001x | B2h | Clock, Unused Clock Output Disable | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| <div>PCI Express Assign</div> <table> <tr> <th>PORT #</th><th>ASSIGNED TO</th></tr> <tr> <td>0</td><td>NC</td></tr> <tr> <td>1</td><td>Mini Card 1 (WLAN)</td></tr> <tr> <td>2</td><td>NC</td></tr> <tr> <td>3</td><td>LOM</td></tr> <tr> <td>4</td><td>NC</td></tr> <tr> <td>5</td><td>Mini Card 2 (ROBSON or DVB-T)</td></tr> </table> | PORT # | ASSIGNED TO | 0 | NC | 1 | Mini Card 1 (WLAN) | 2 | NC | 3 | LOM | 4 | NC | 5 | Mini Card 2 (ROBSON or DVB-T) | | | | <div>REVISION HISTORY</div> <p>See rev notes for more information.</p> | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| PORT # | ASSIGNED TO | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 0 | NC | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 1 | Mini Card 1 (WLAN) | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 2 | NC | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 3 | LOM | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 4 | NC | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 5 | Mini Card 2 (ROBSON or DVB-T) | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | | | | <table> <tr> <td>DRAW</td><td>WK.Yeo</td><td>DATE</td><td>12/05/2008</td><td>TITLE</td><td rowspan="3">CANNES MAIN BOARD INFO</td><td rowspan="3">SAMSUNG ELECTRONICS</td></tr> <tr> <td>CHECK</td><td>SH.Lee</td><td>DEV. STEP</td><td>PV</td><td></td></tr> <tr> <td>APPROVAL</td><td>KK.Bin</td><td>REV</td><td>REV 1.0</td><td></td></tr> <tr> <td>MODULE CODE</td><td>undefined</td><td>LAST EDIT</td><td></td><td></td><td></td><td>PART NO. BA41-01060A</td></tr> <tr> <td></td><td></td><td></td><td></td><td>March 10, 2009 07:55:21 AM</td><td>PAGE</td><td>3 OF 67</td></tr> </table> | DRAW | WK.Yeo | DATE | 12/05/2008 | TITLE | CANNES MAIN BOARD INFO | SAMSUNG ELECTRONICS | CHECK | SH.Lee | DEV. STEP | PV | | APPROVAL | KK.Bin | REV | REV 1.0 | | MODULE CODE | undefined | LAST EDIT | | | | PART NO. BA41-01060A | | | | | March 10, 2009 07:55:21 AM | PAGE | 3 OF 67 | | | | | | | | | | | | | | | | | | | | | | | | | | |
| DRAW | WK.Yeo | DATE | 12/05/2008 | TITLE | CANNES MAIN BOARD INFO | SAMSUNG ELECTRONICS | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| CHECK | SH.Lee | DEV. STEP | PV | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| APPROVAL | KK.Bin | REV | REV 1.0 | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| MODULE CODE | undefined | LAST EDIT | | | | PART NO. BA41-01060A | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| | | | | March 10, 2009 07:55:21 AM | PAGE | 3 OF 67 | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |

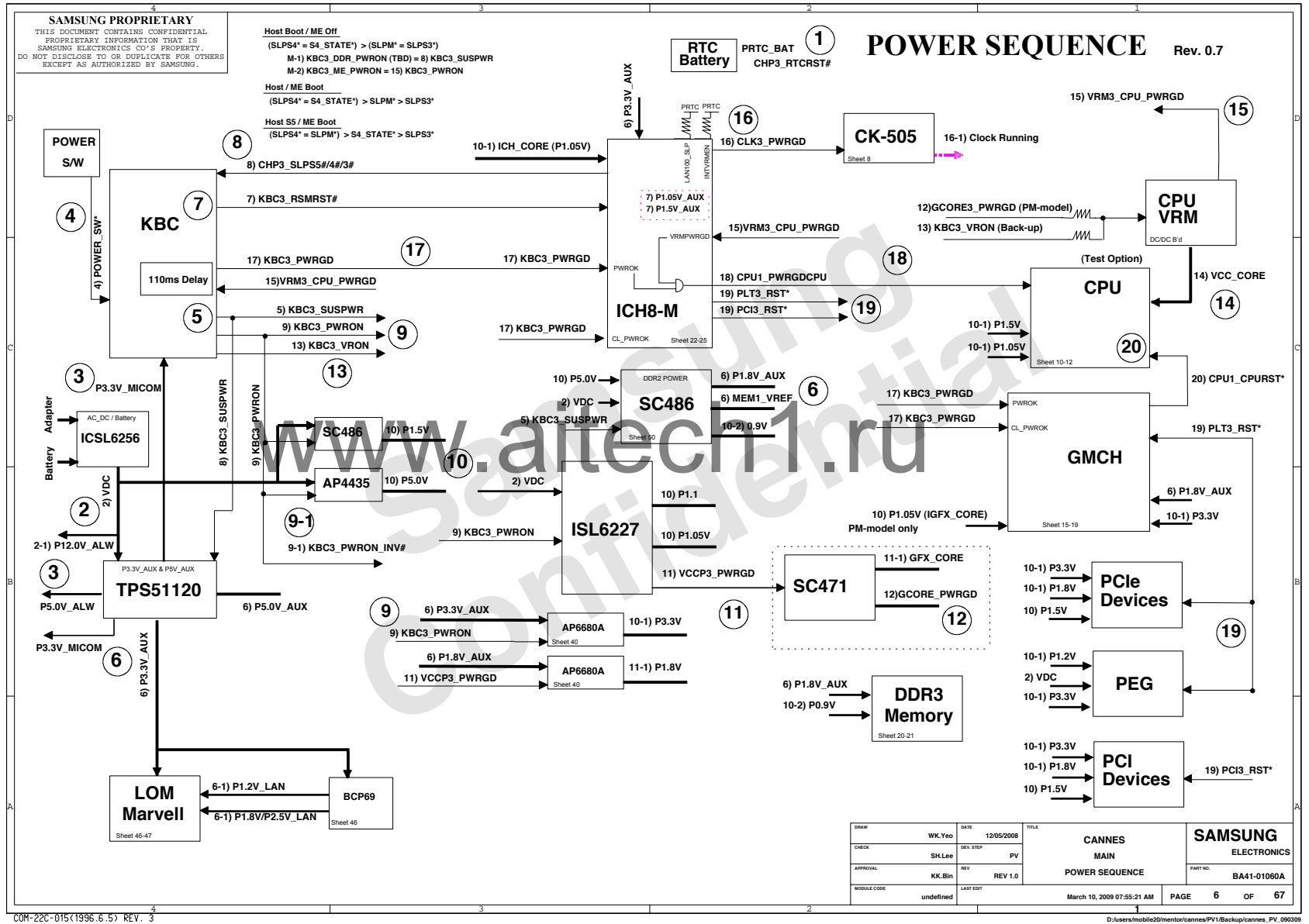
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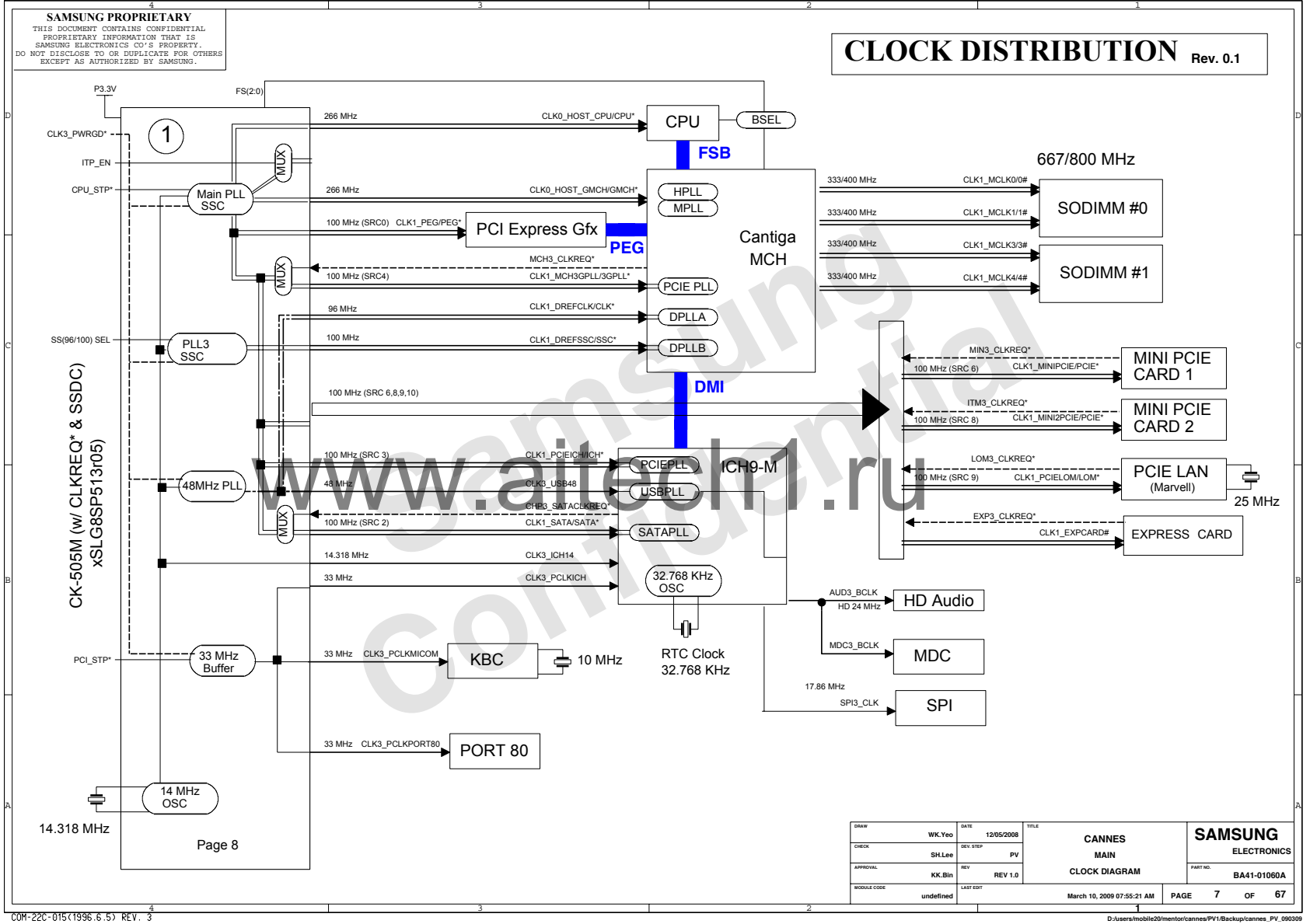
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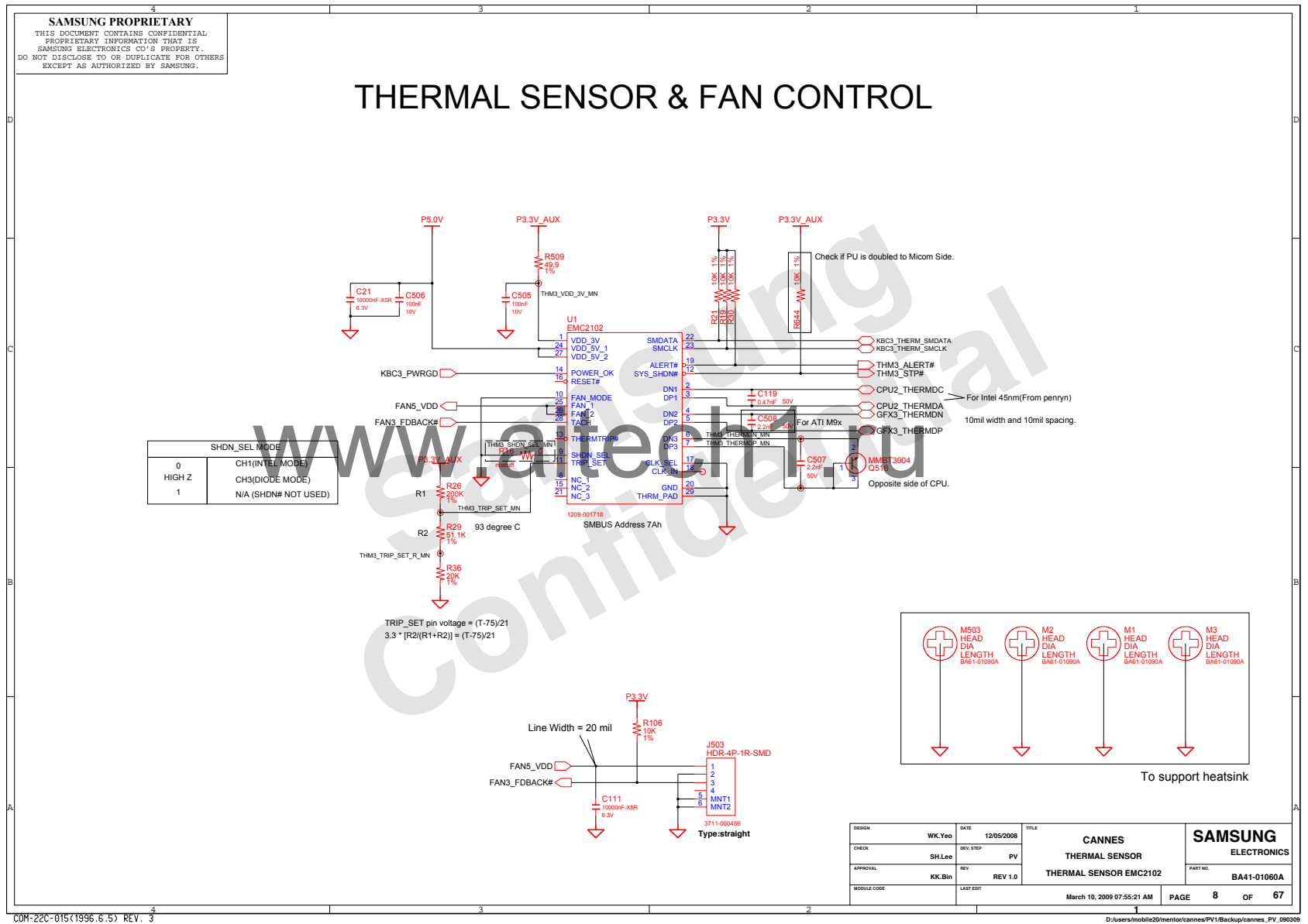
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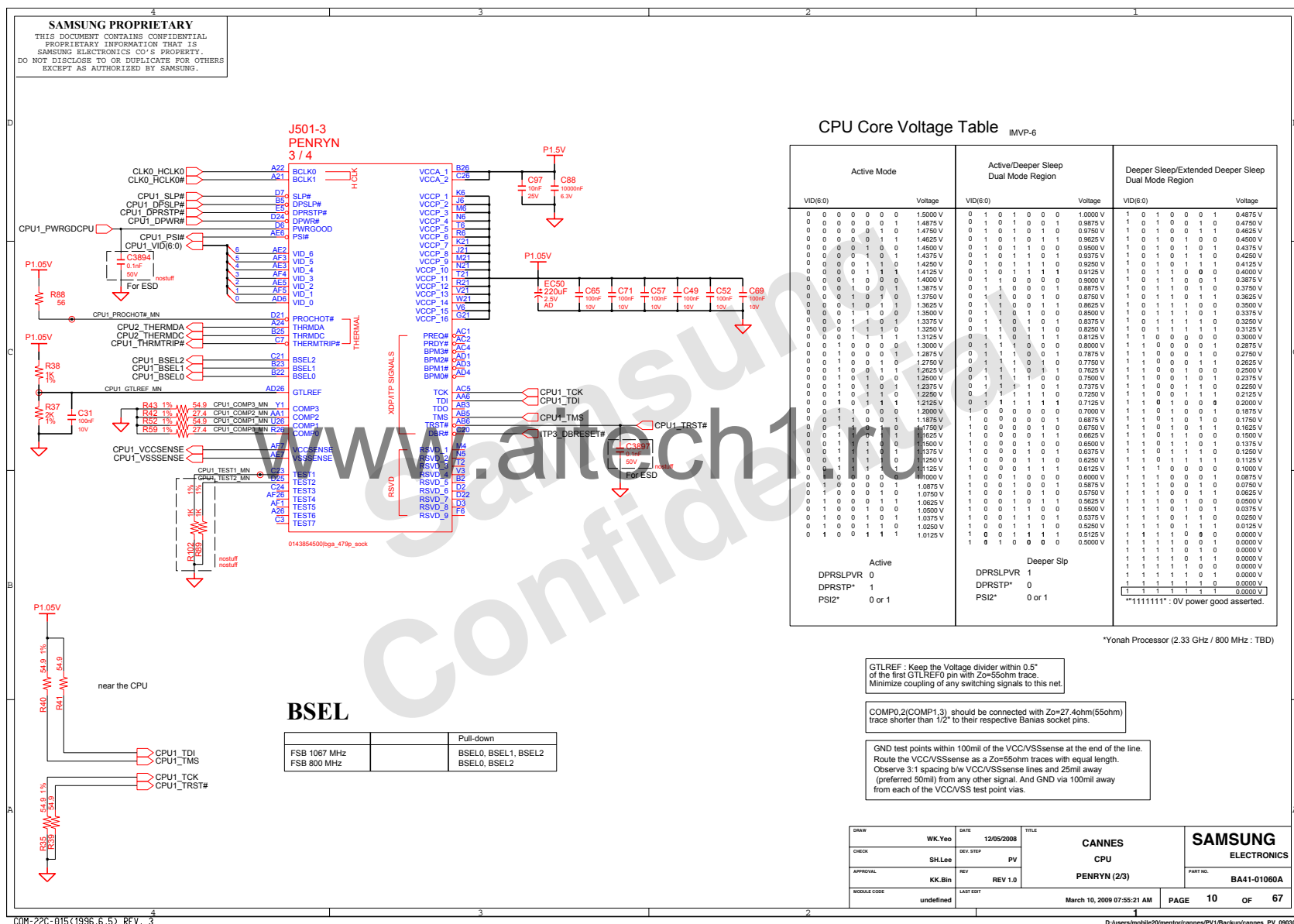
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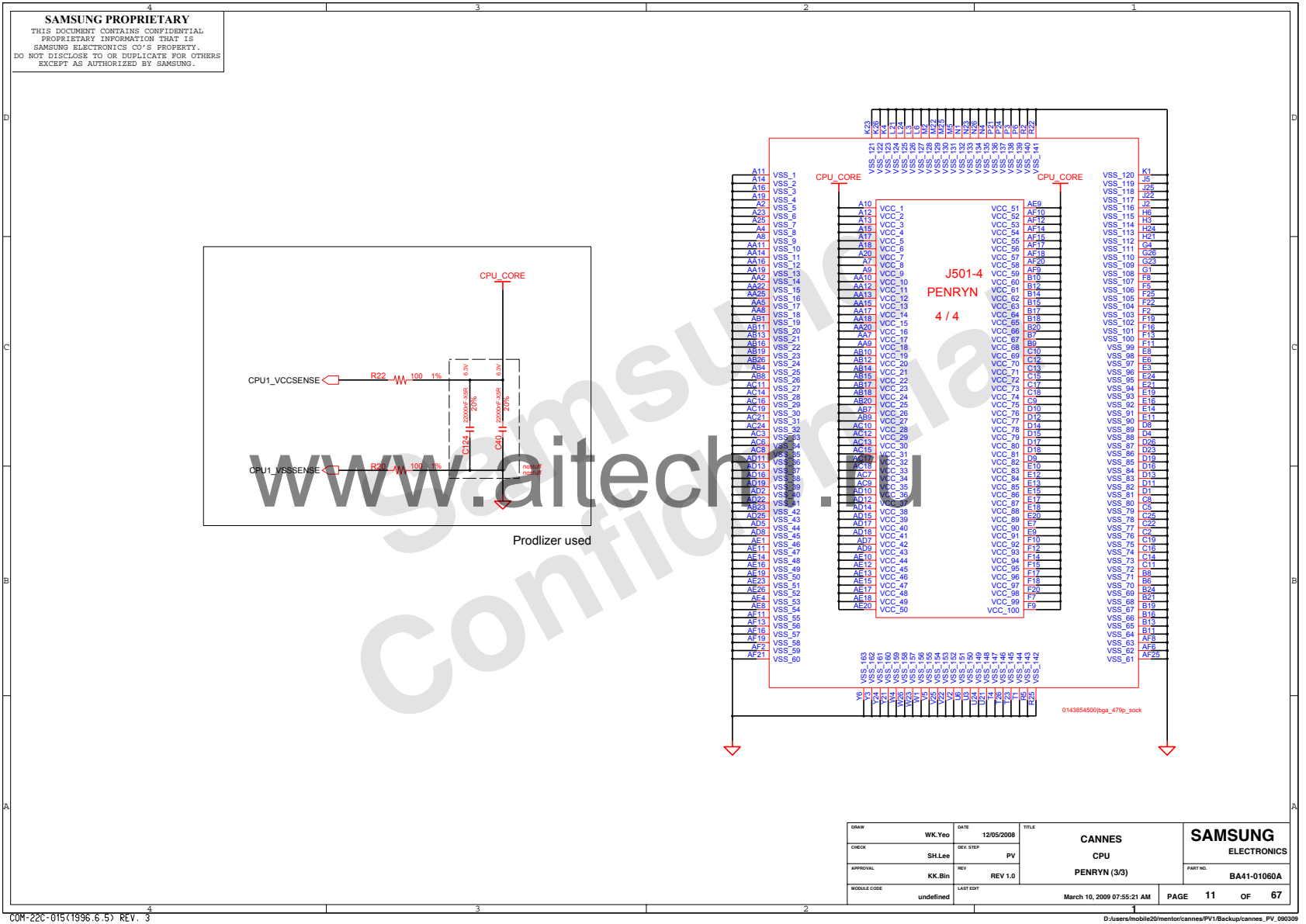


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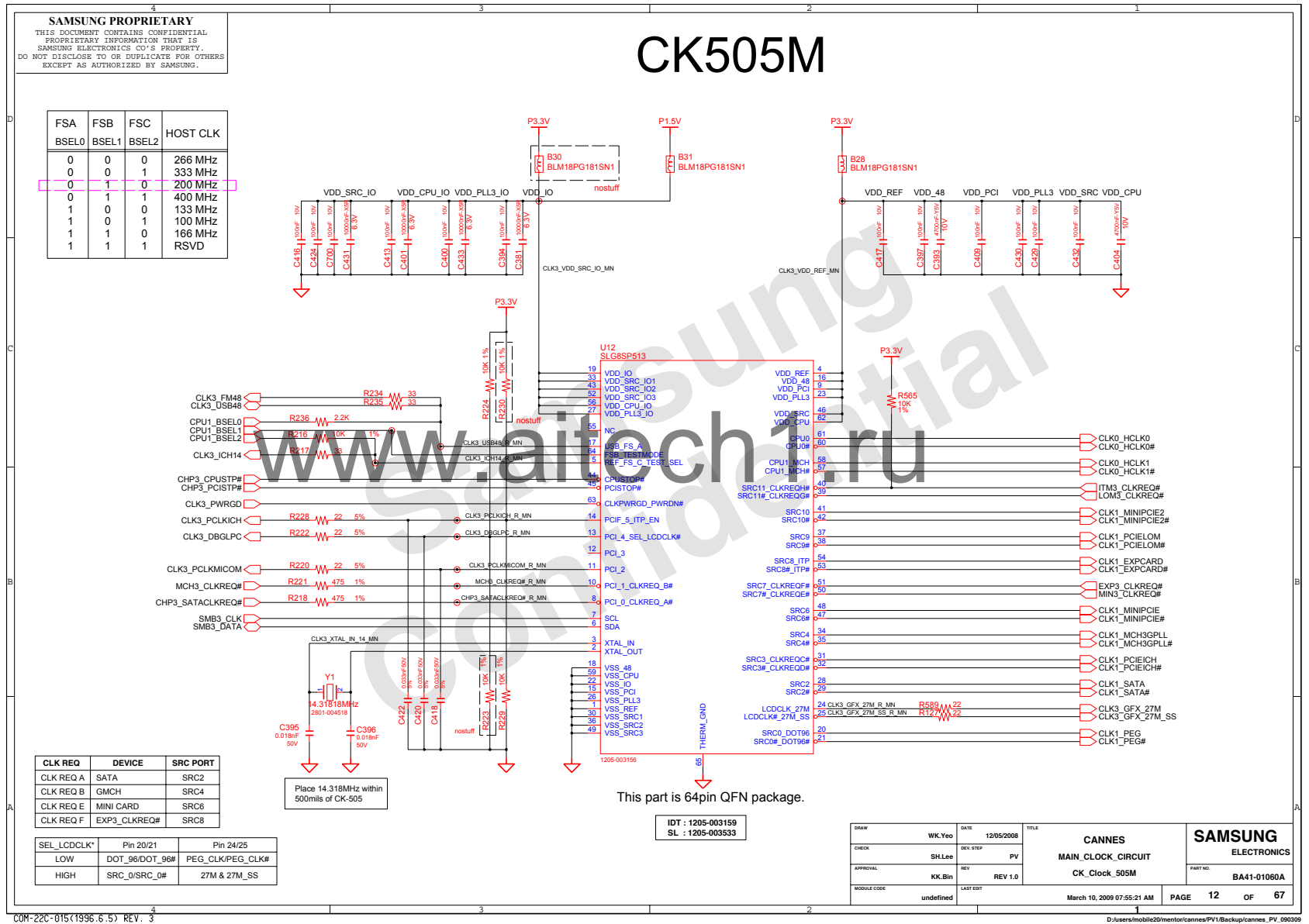
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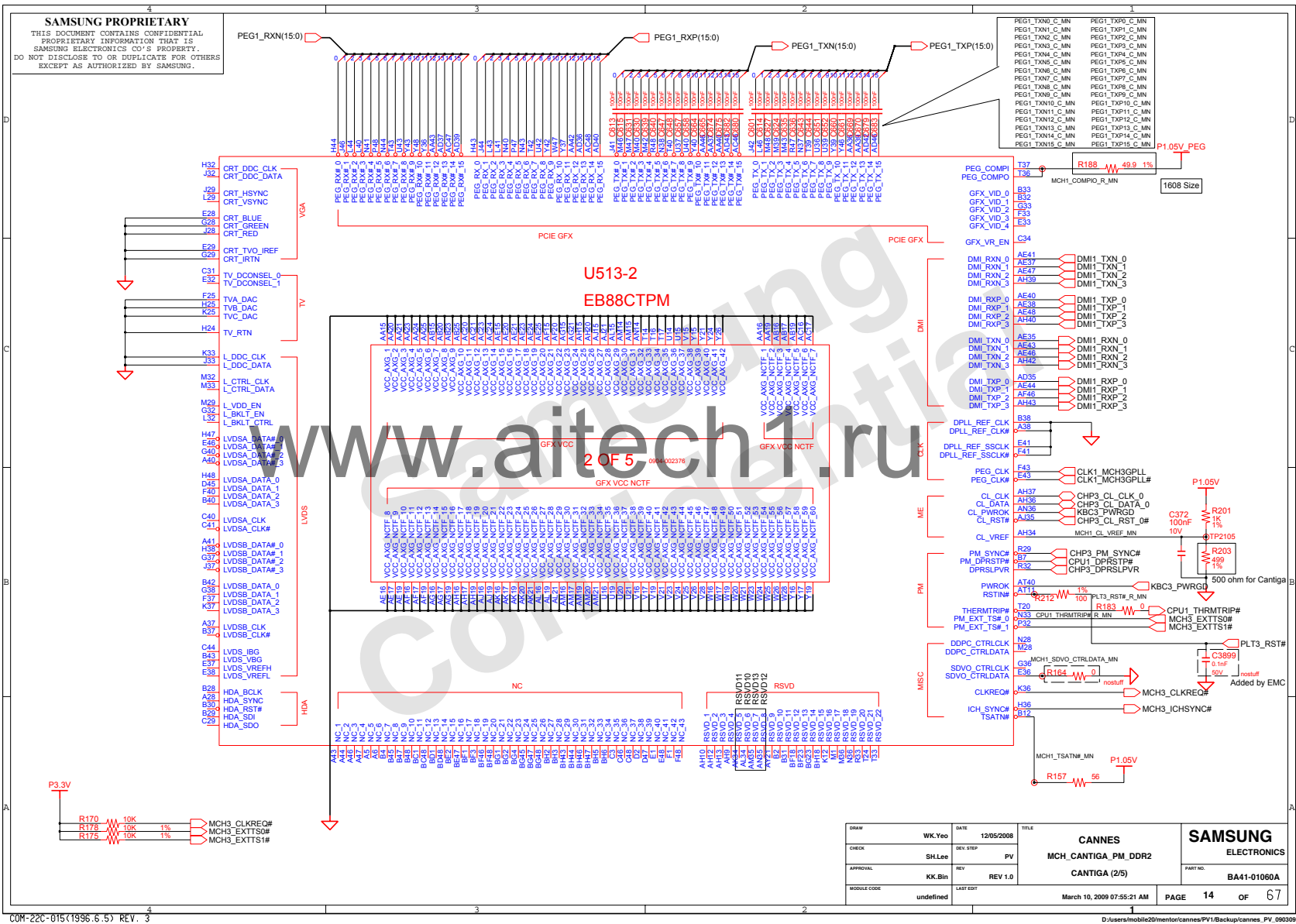


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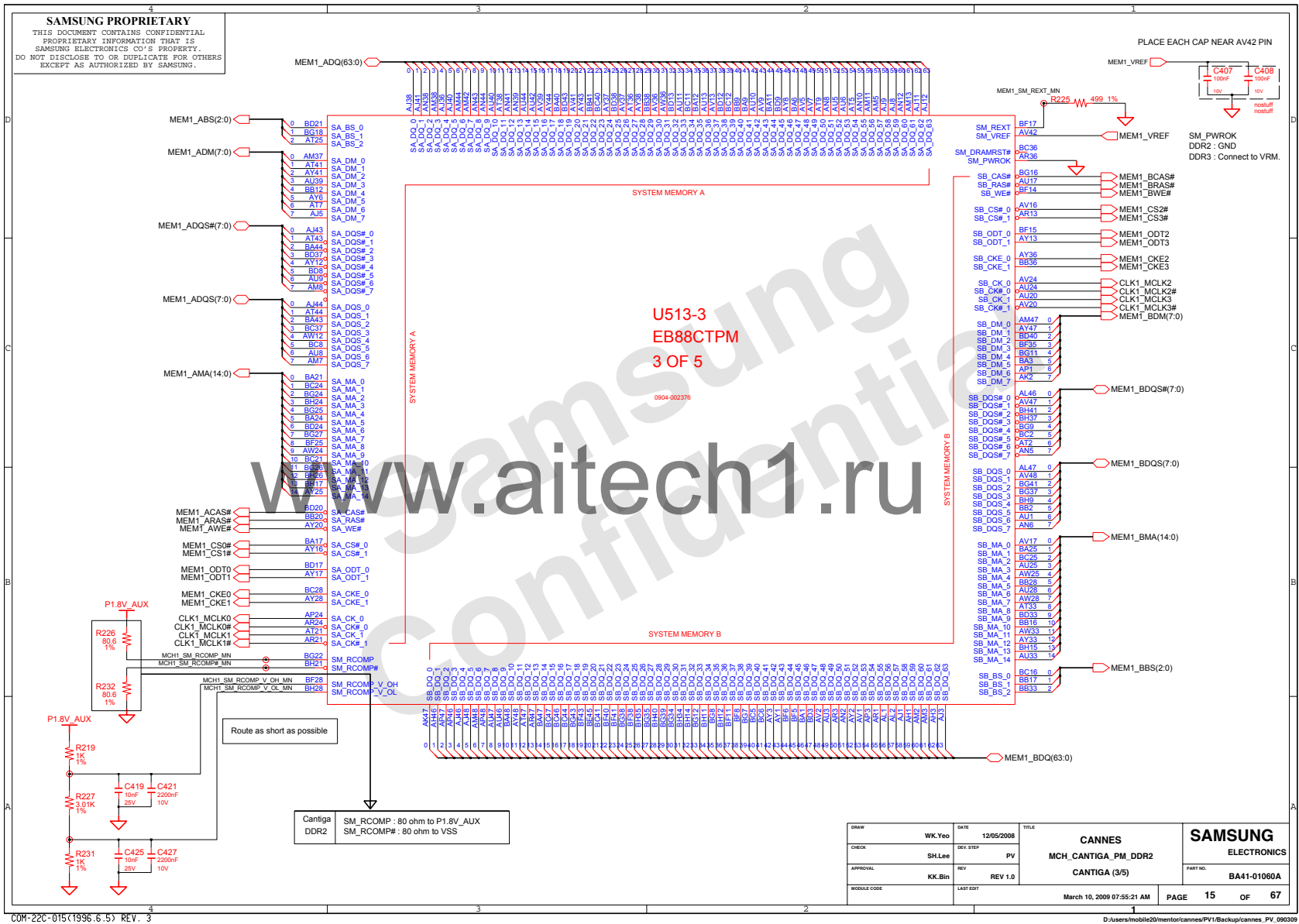




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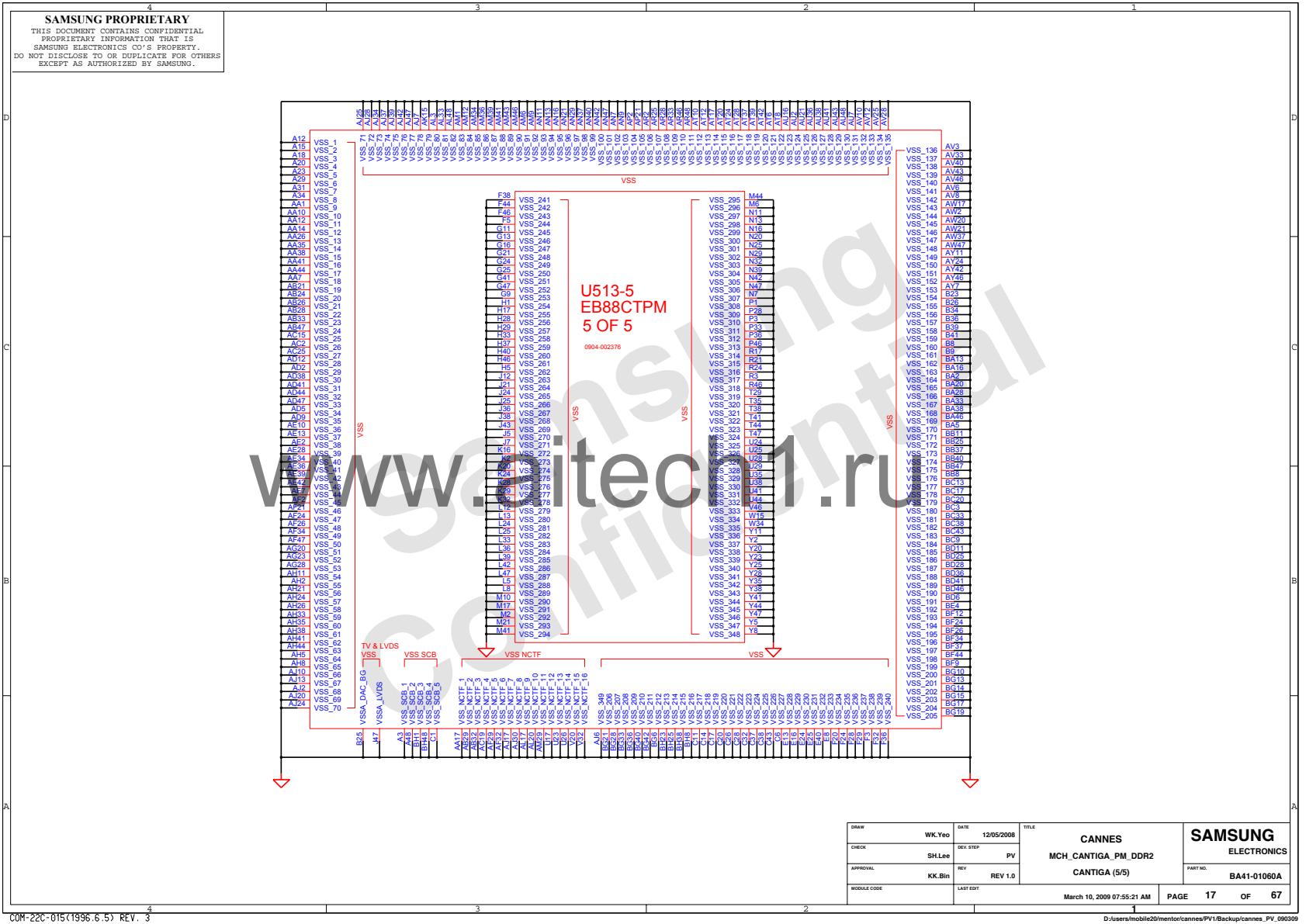


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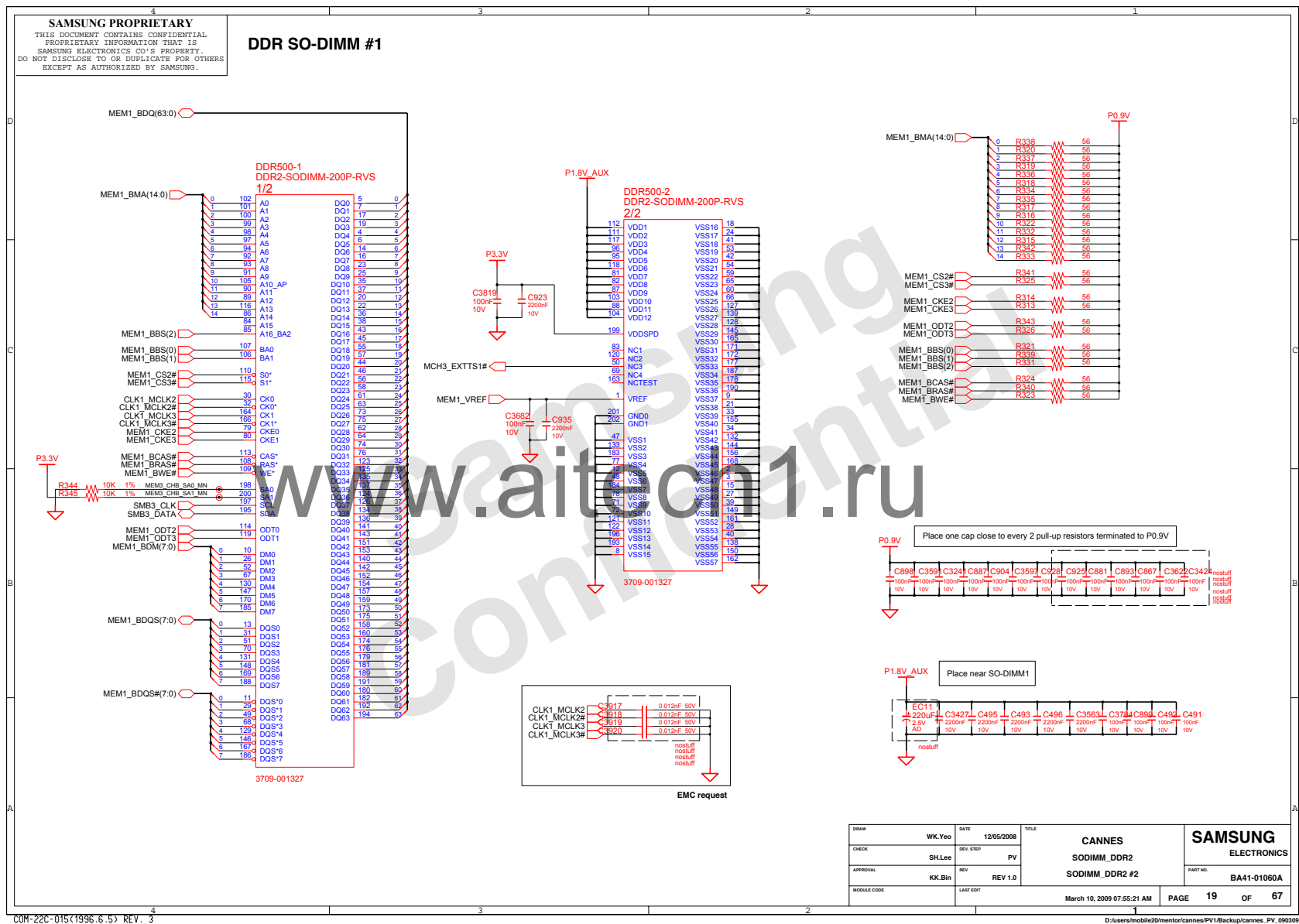


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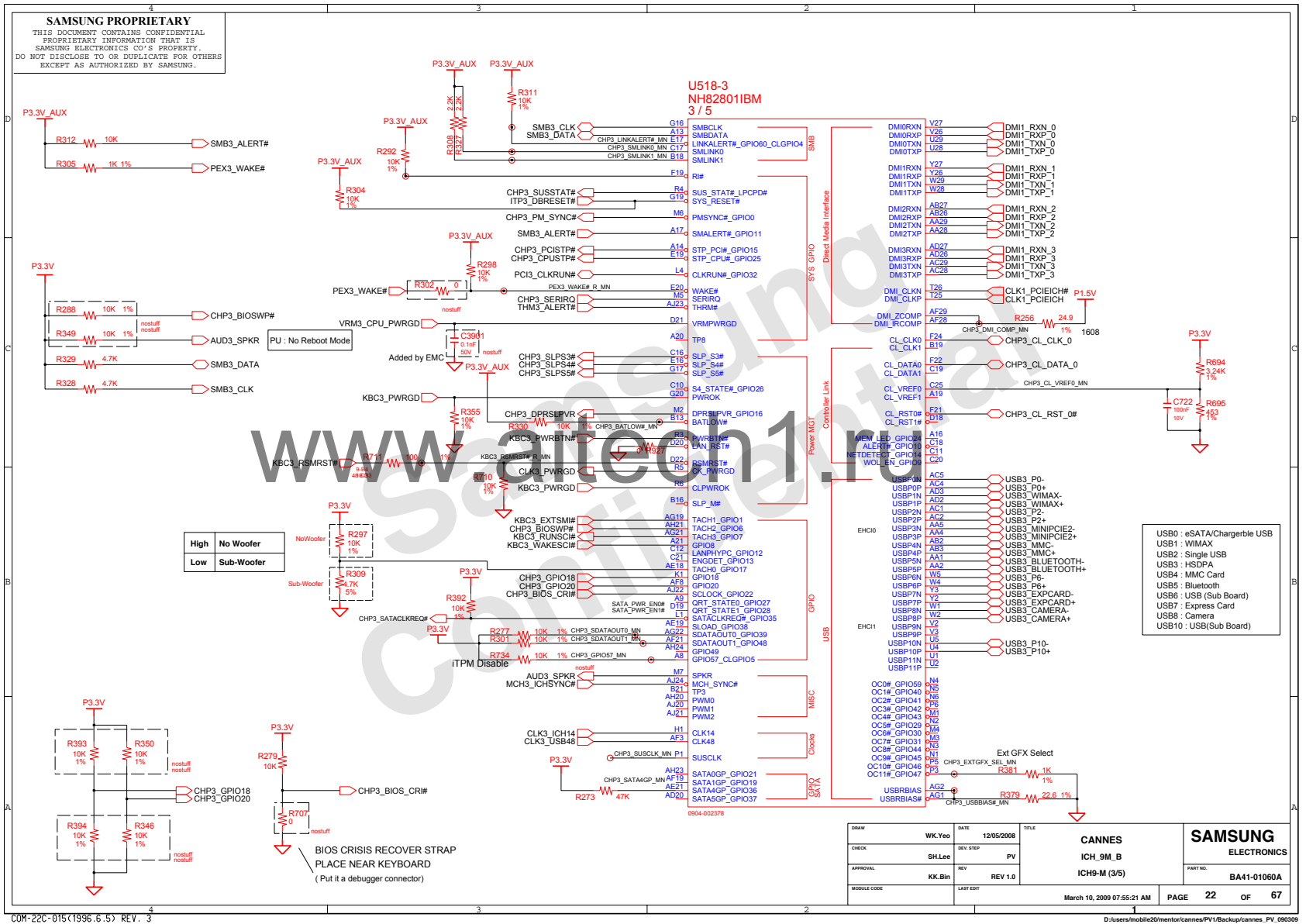
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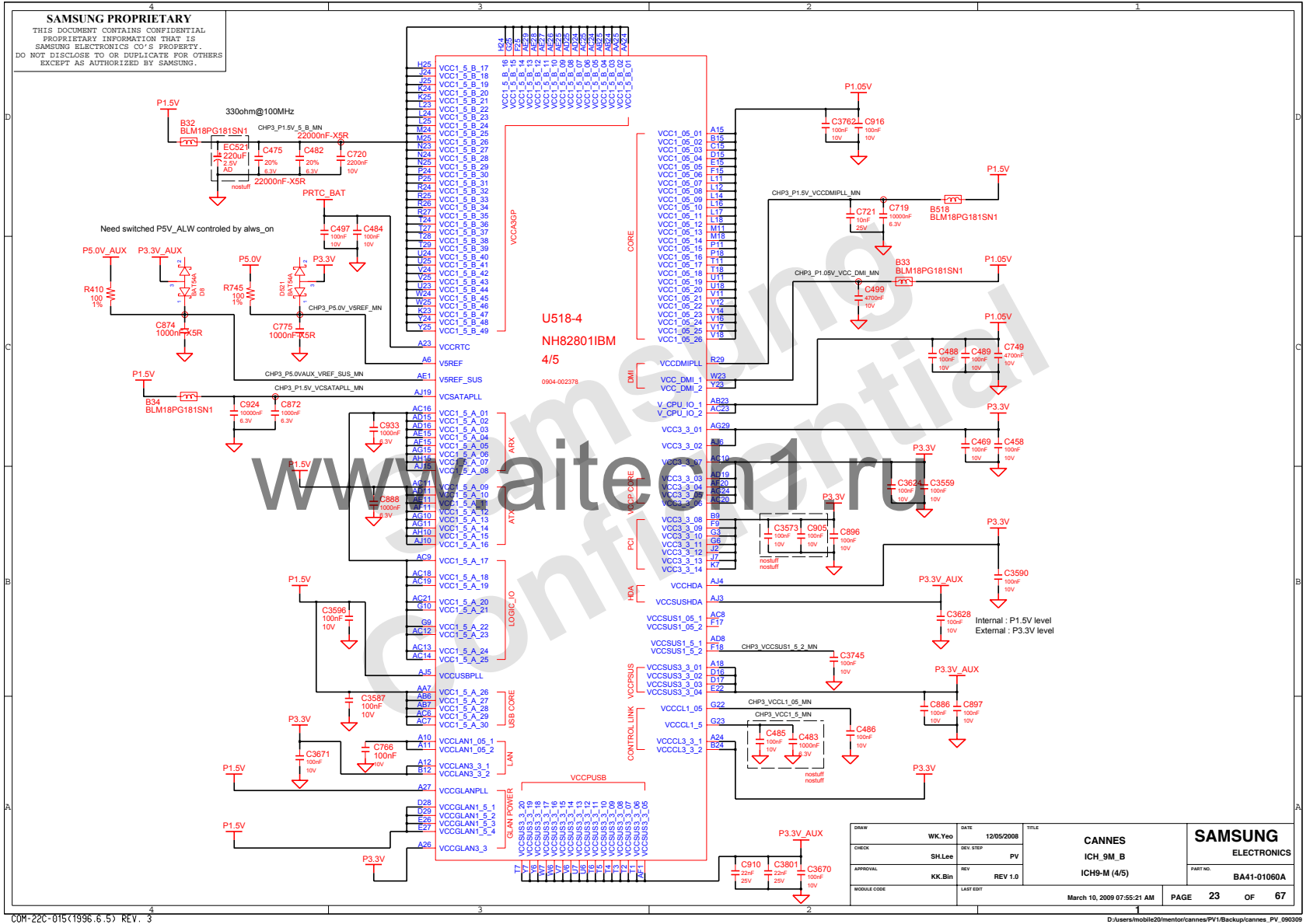




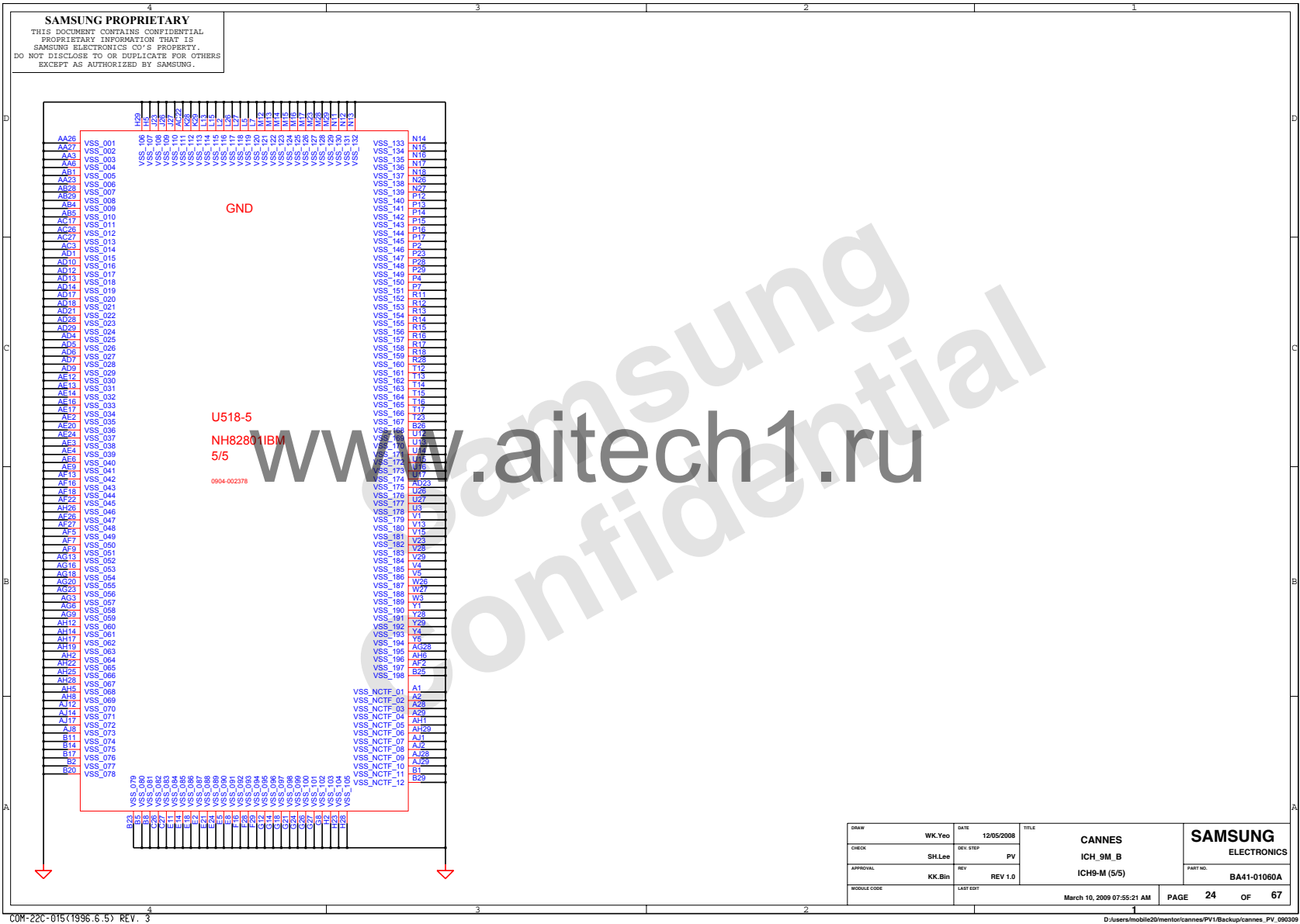
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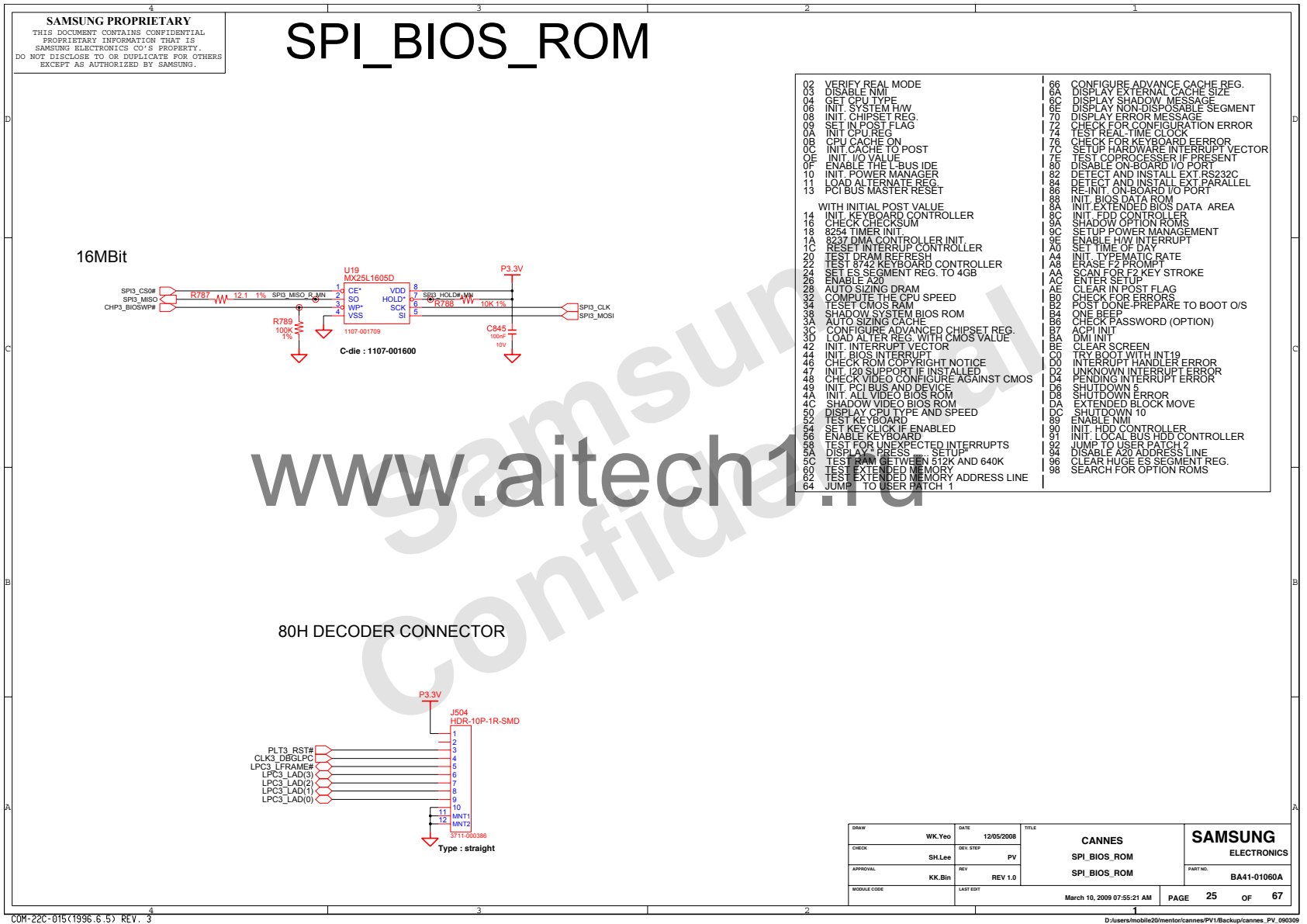
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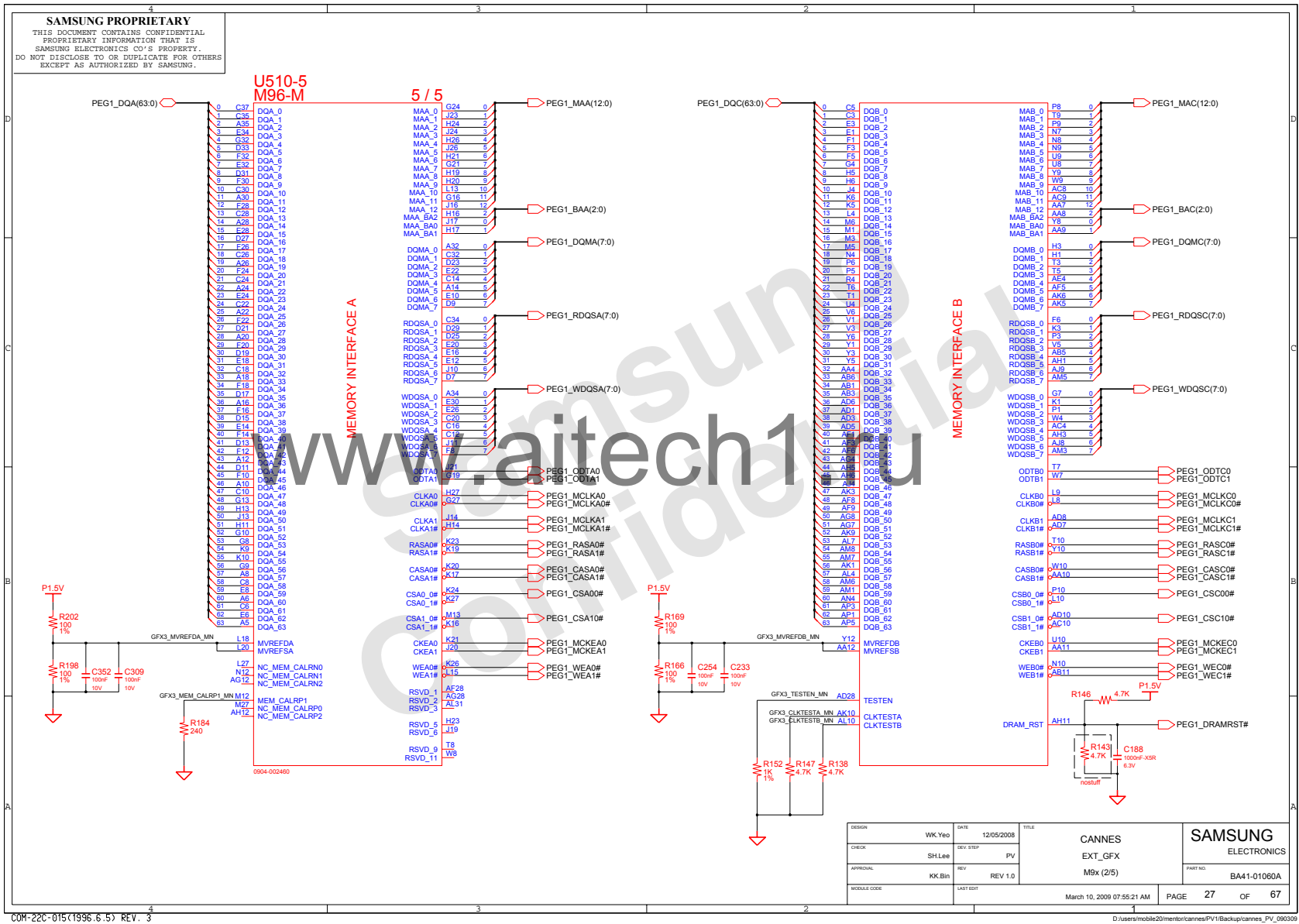
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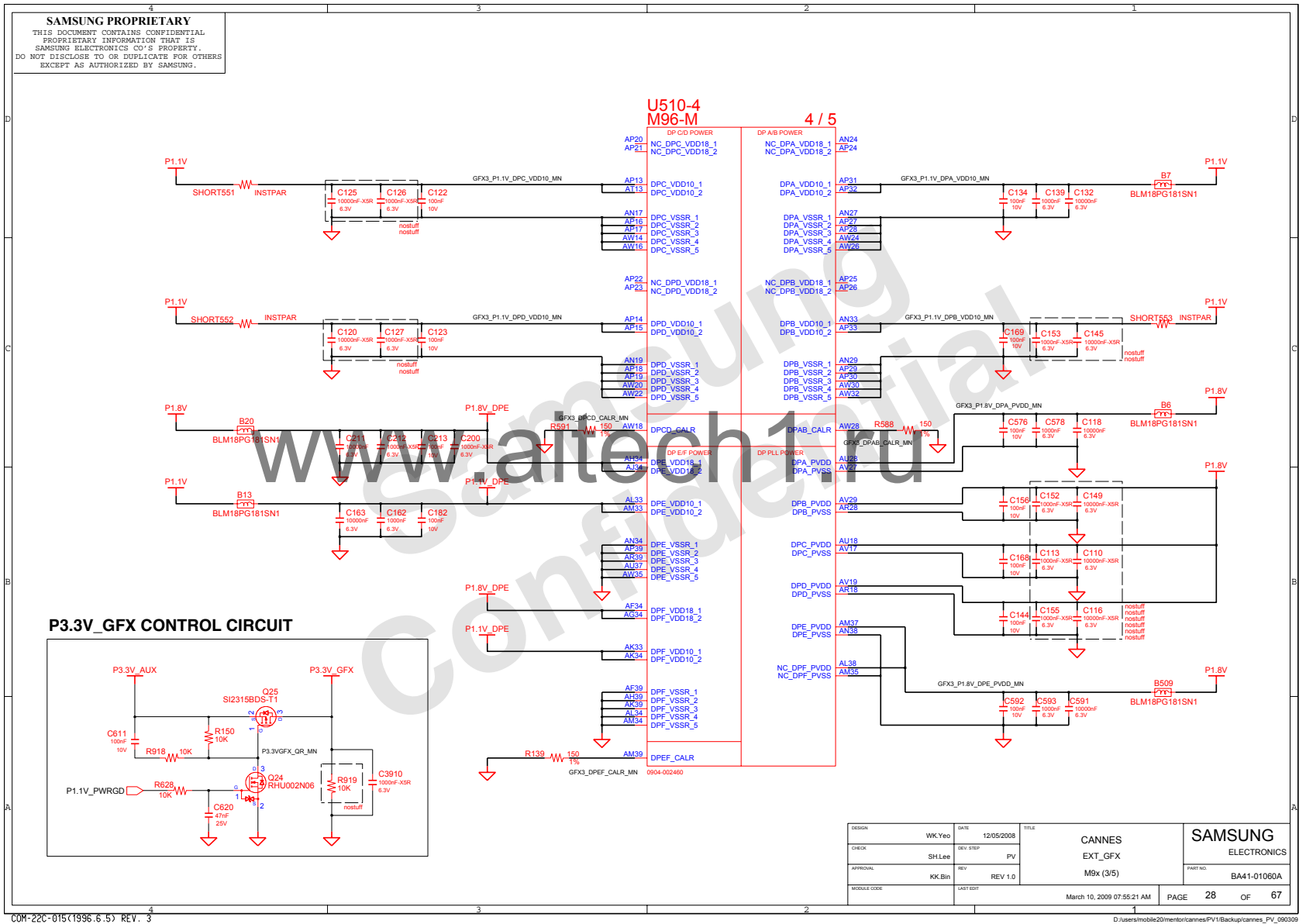
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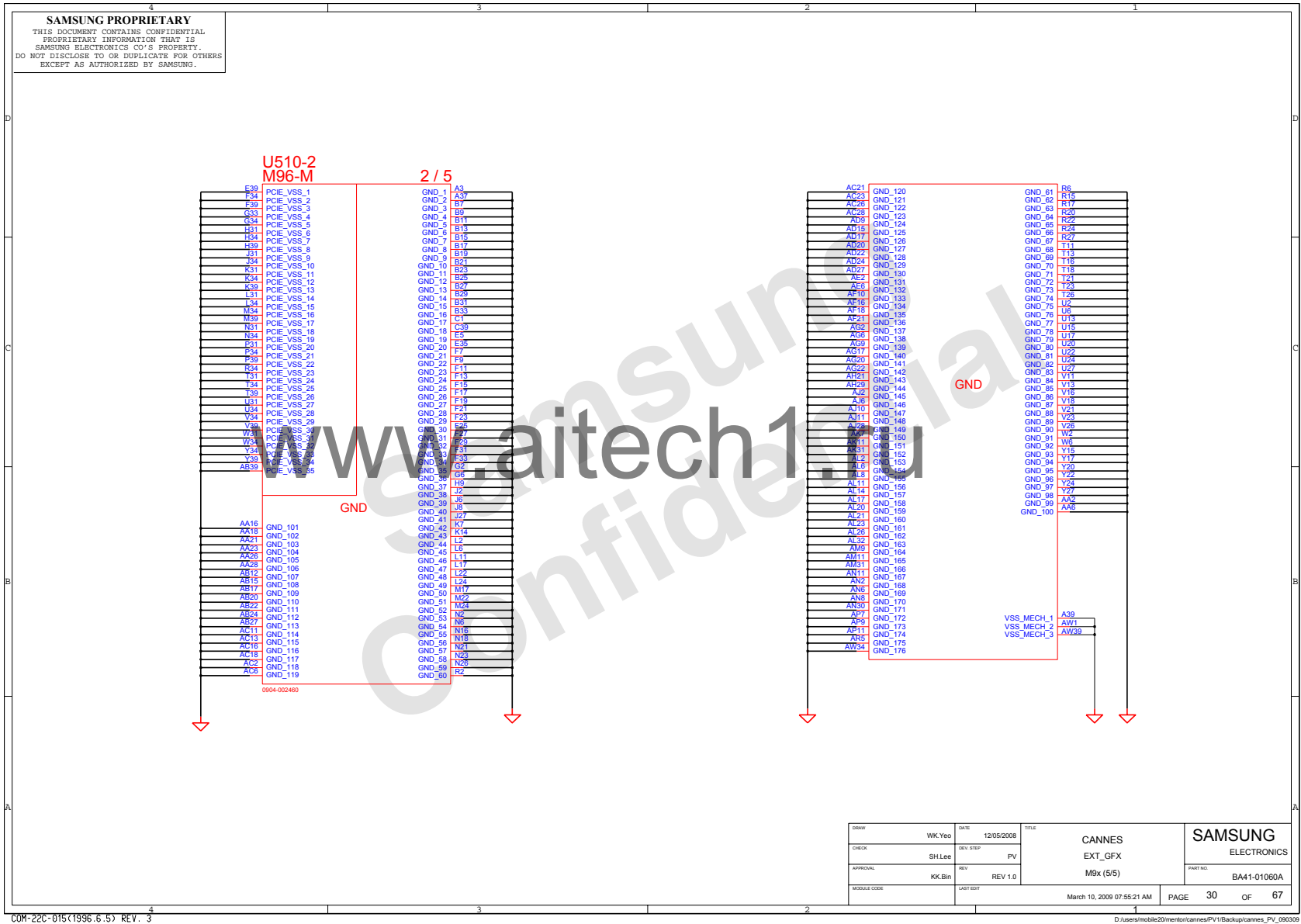


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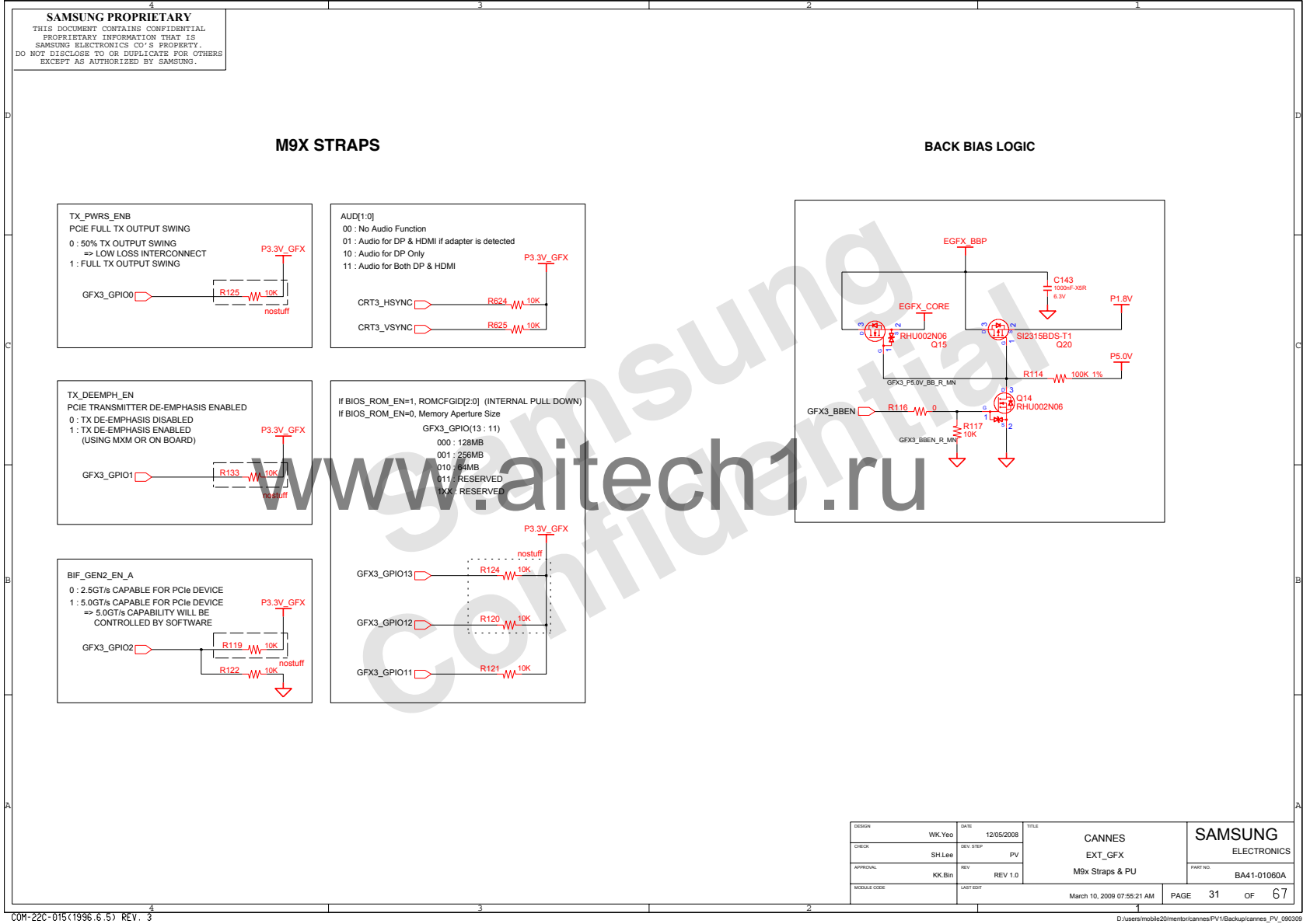




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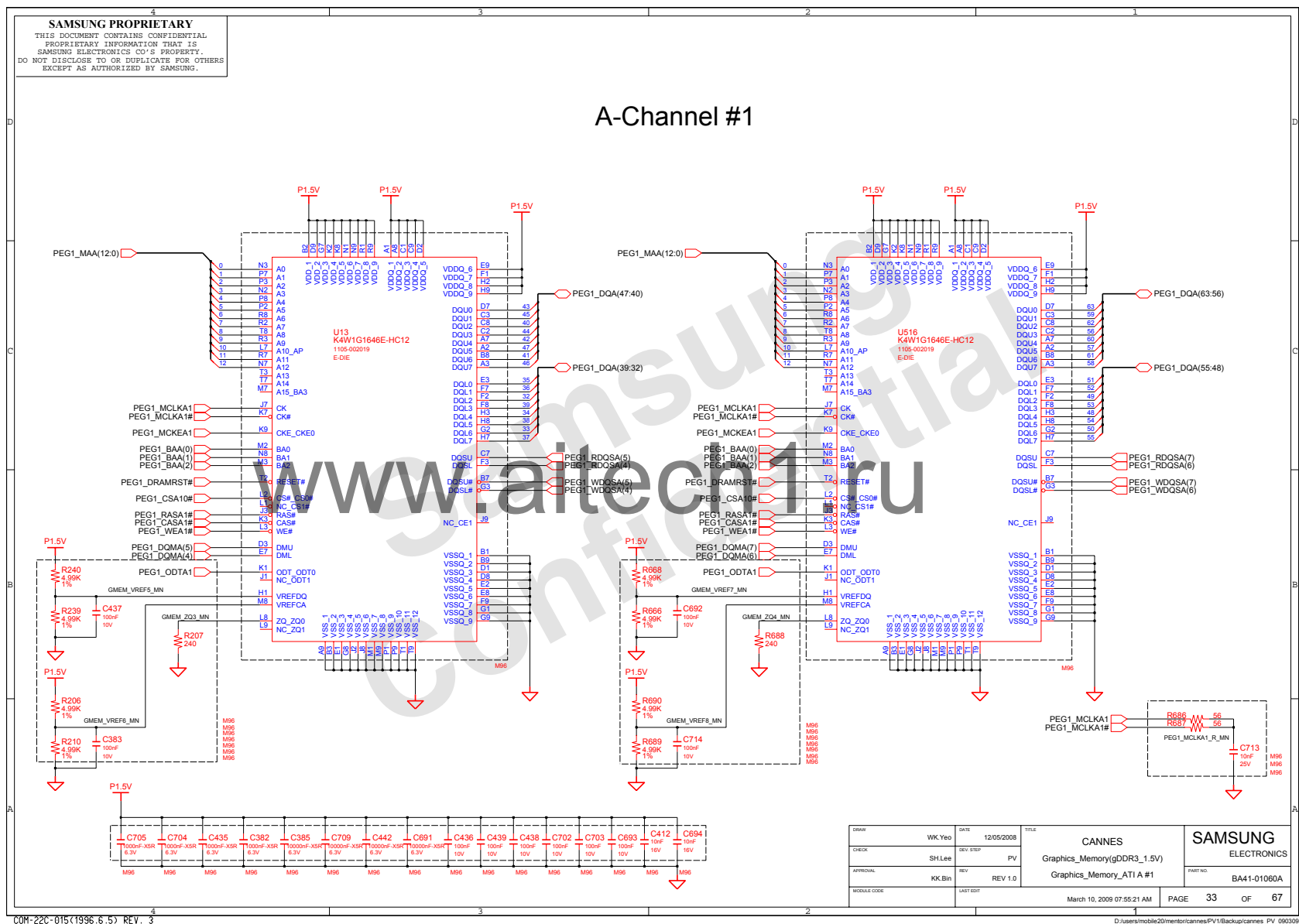


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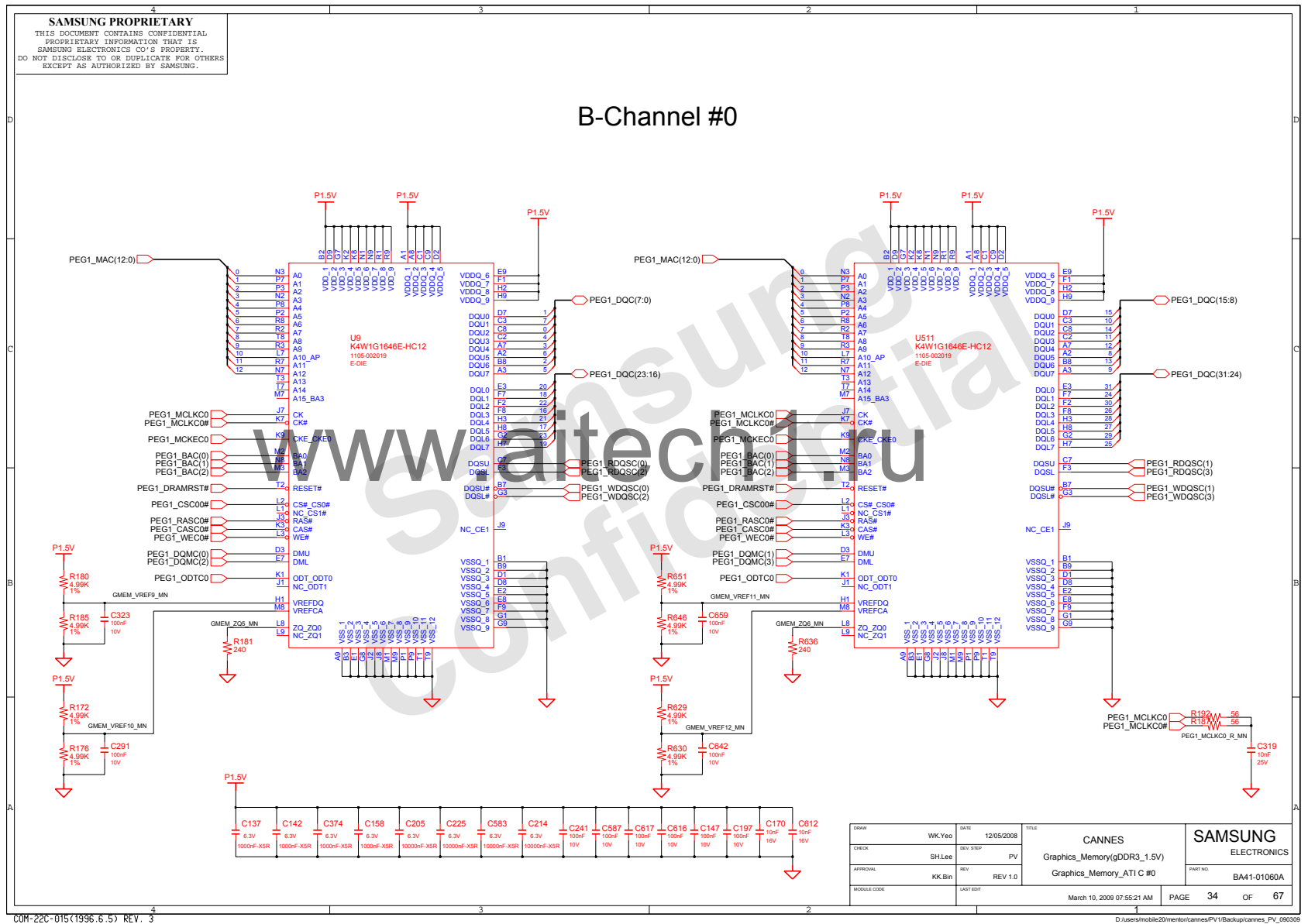




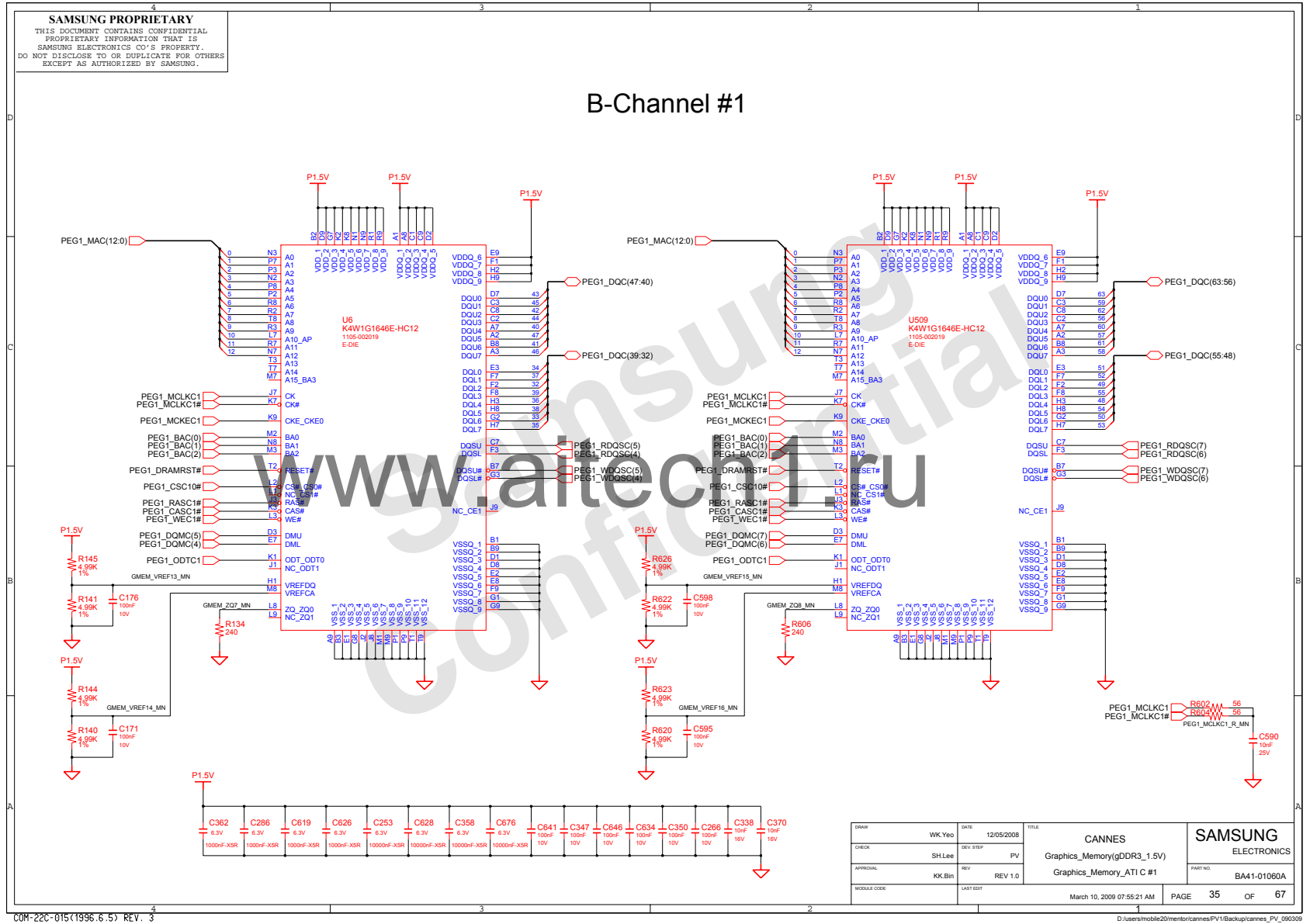
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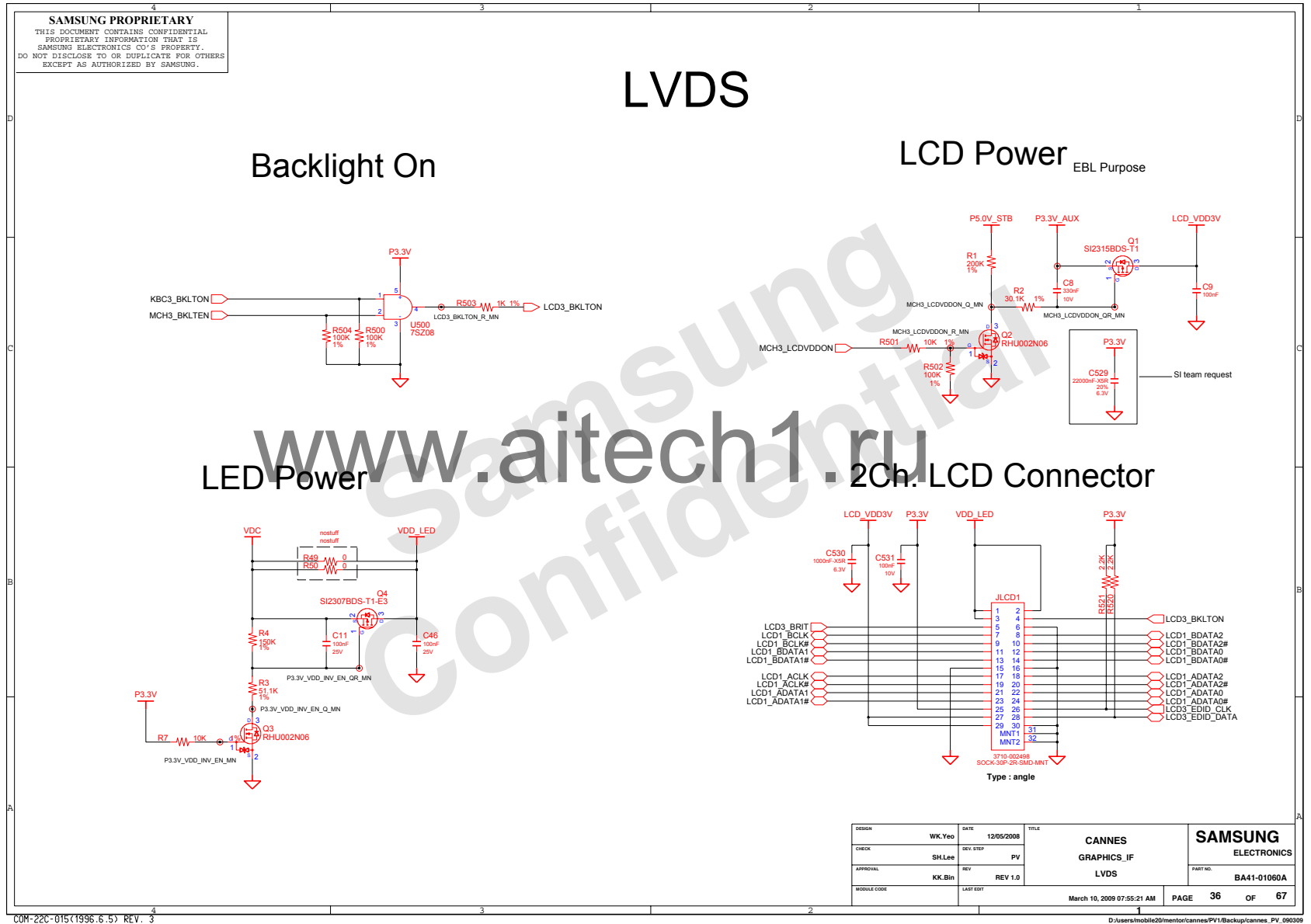
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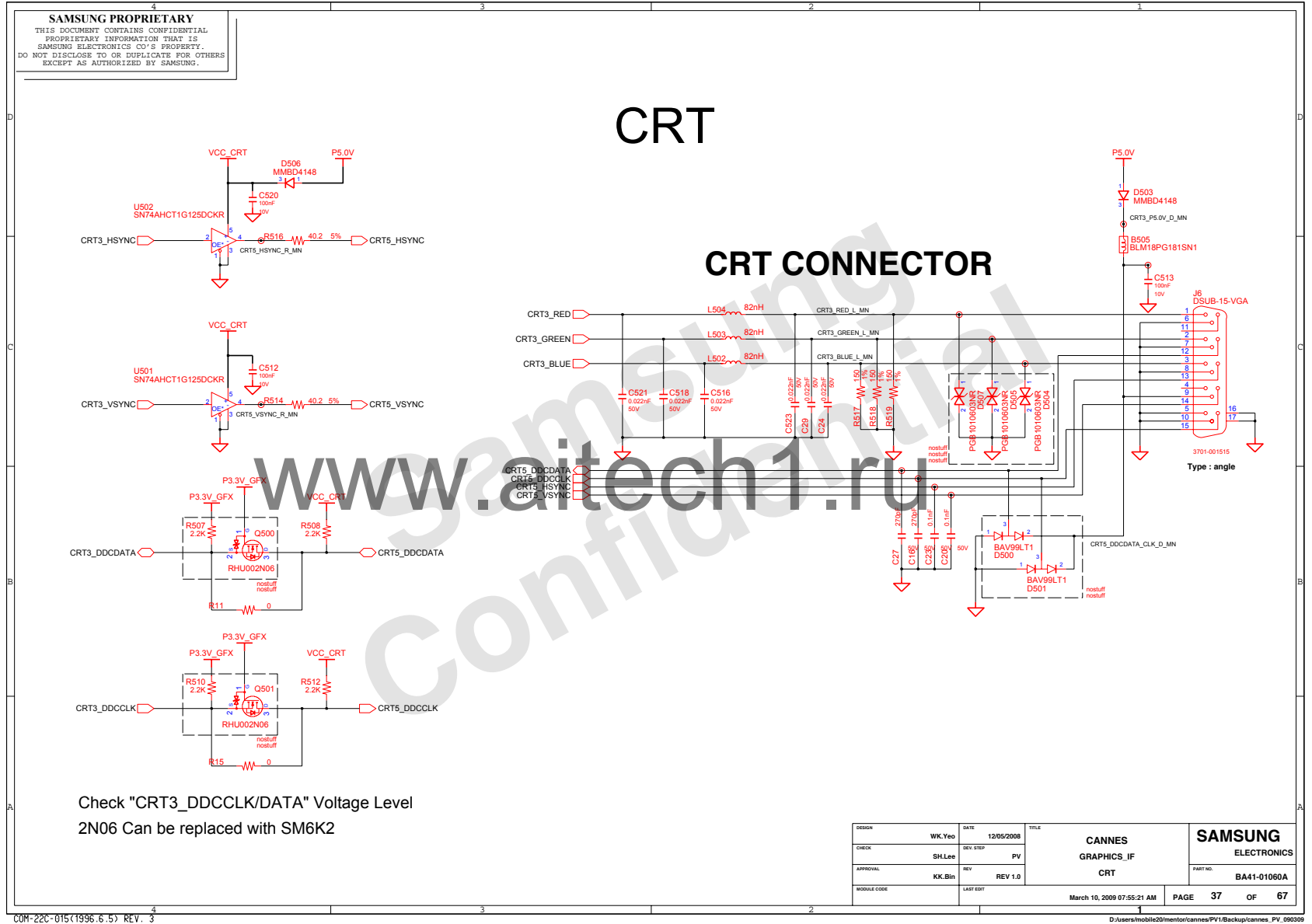
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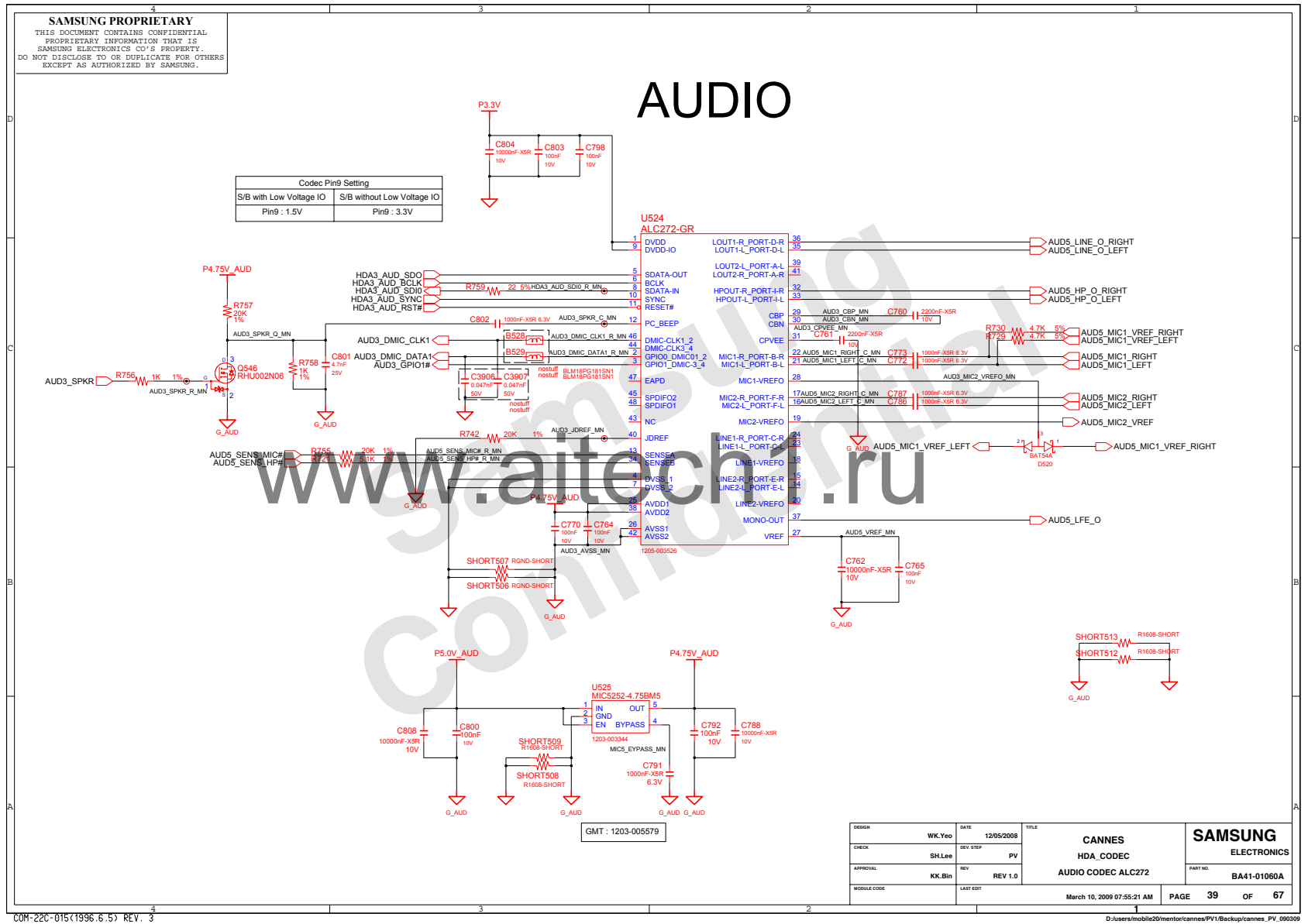


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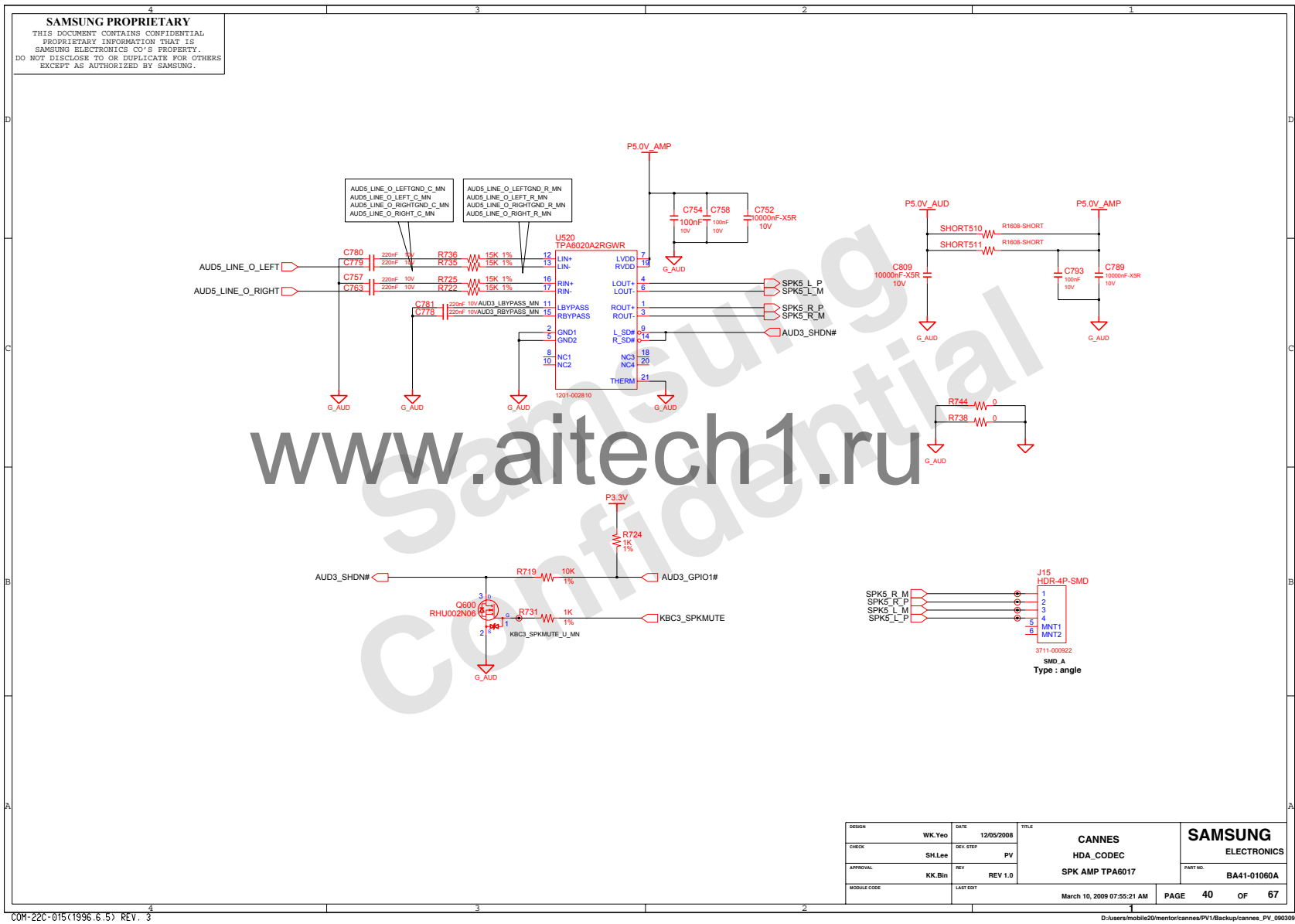




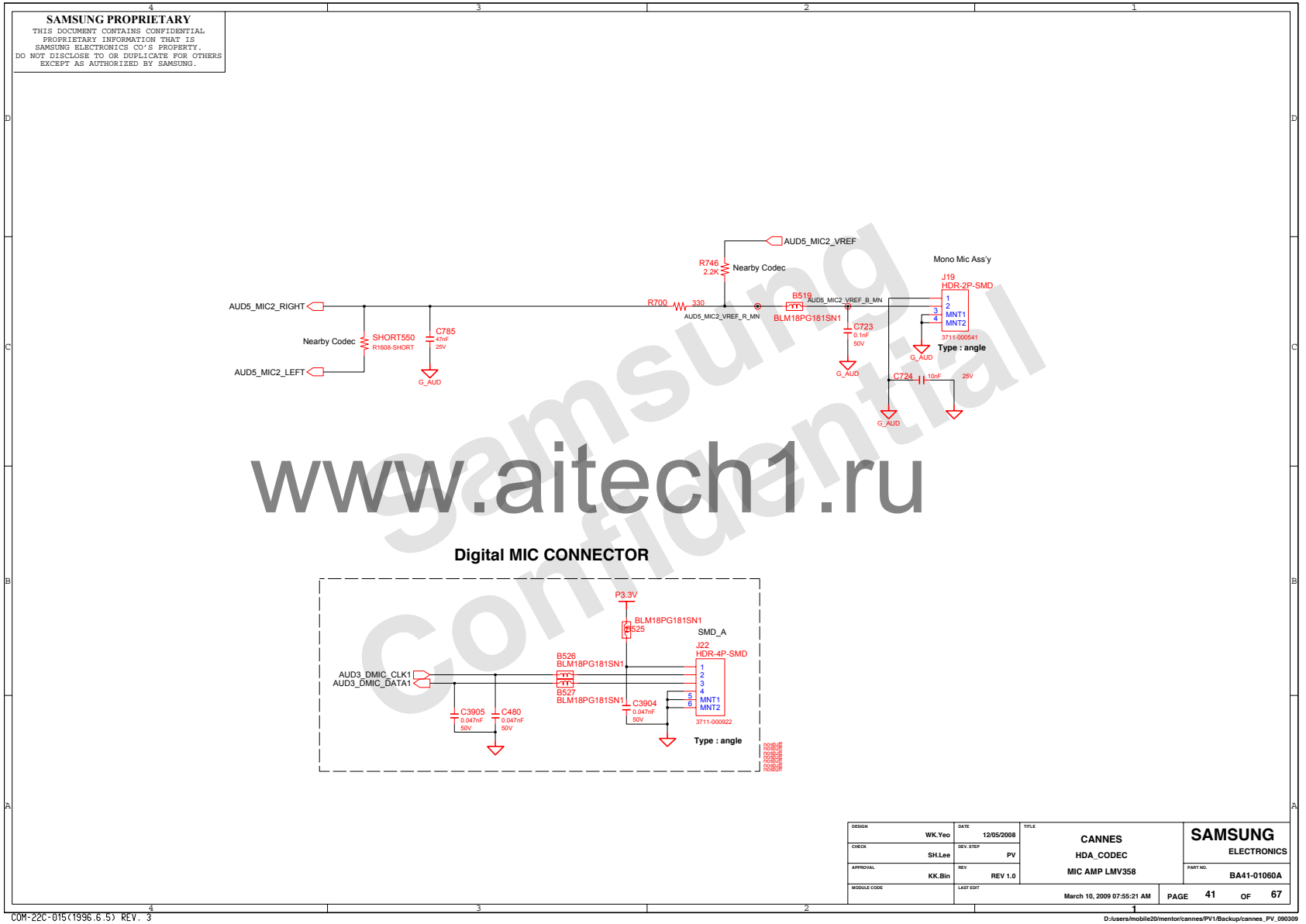
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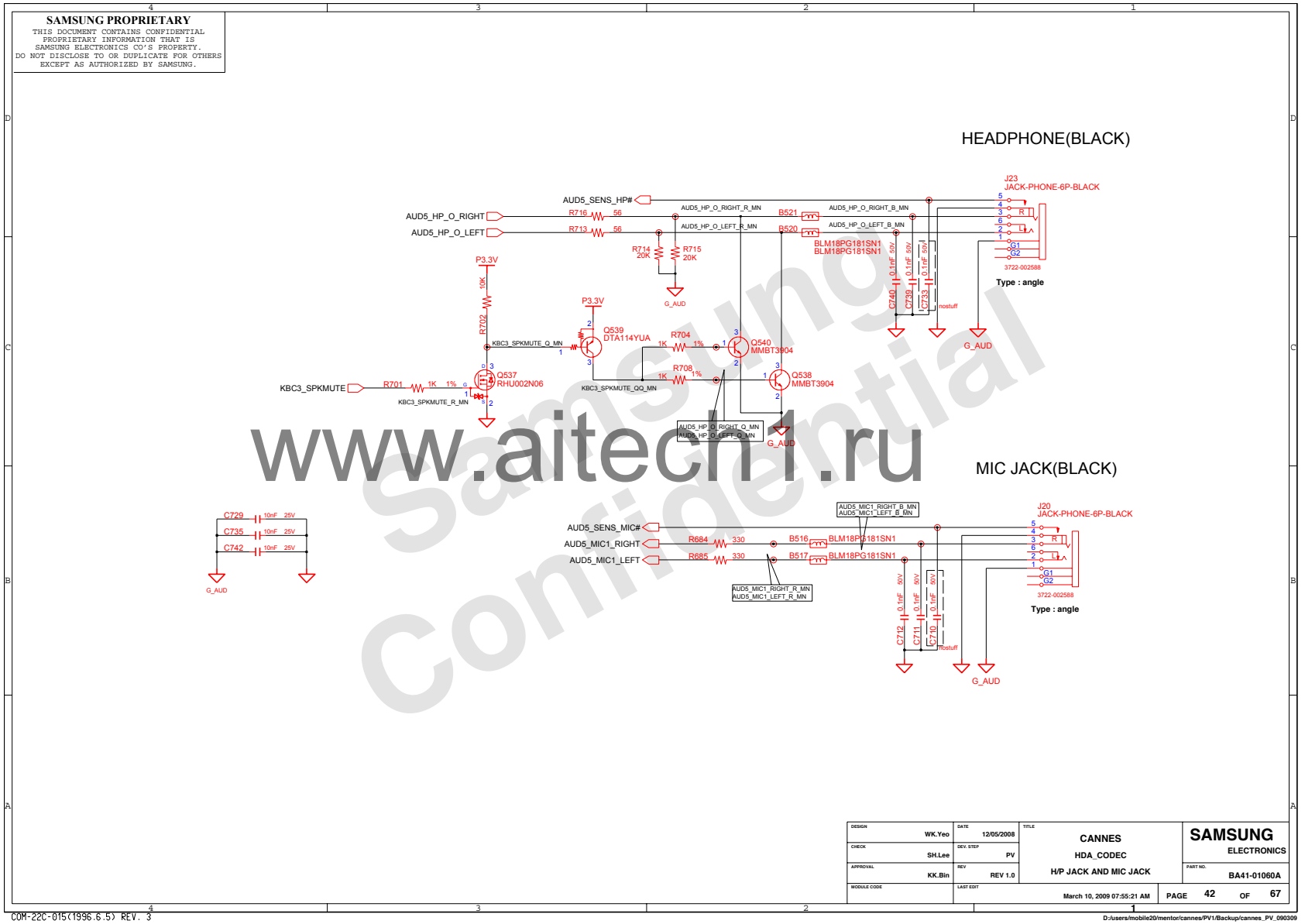
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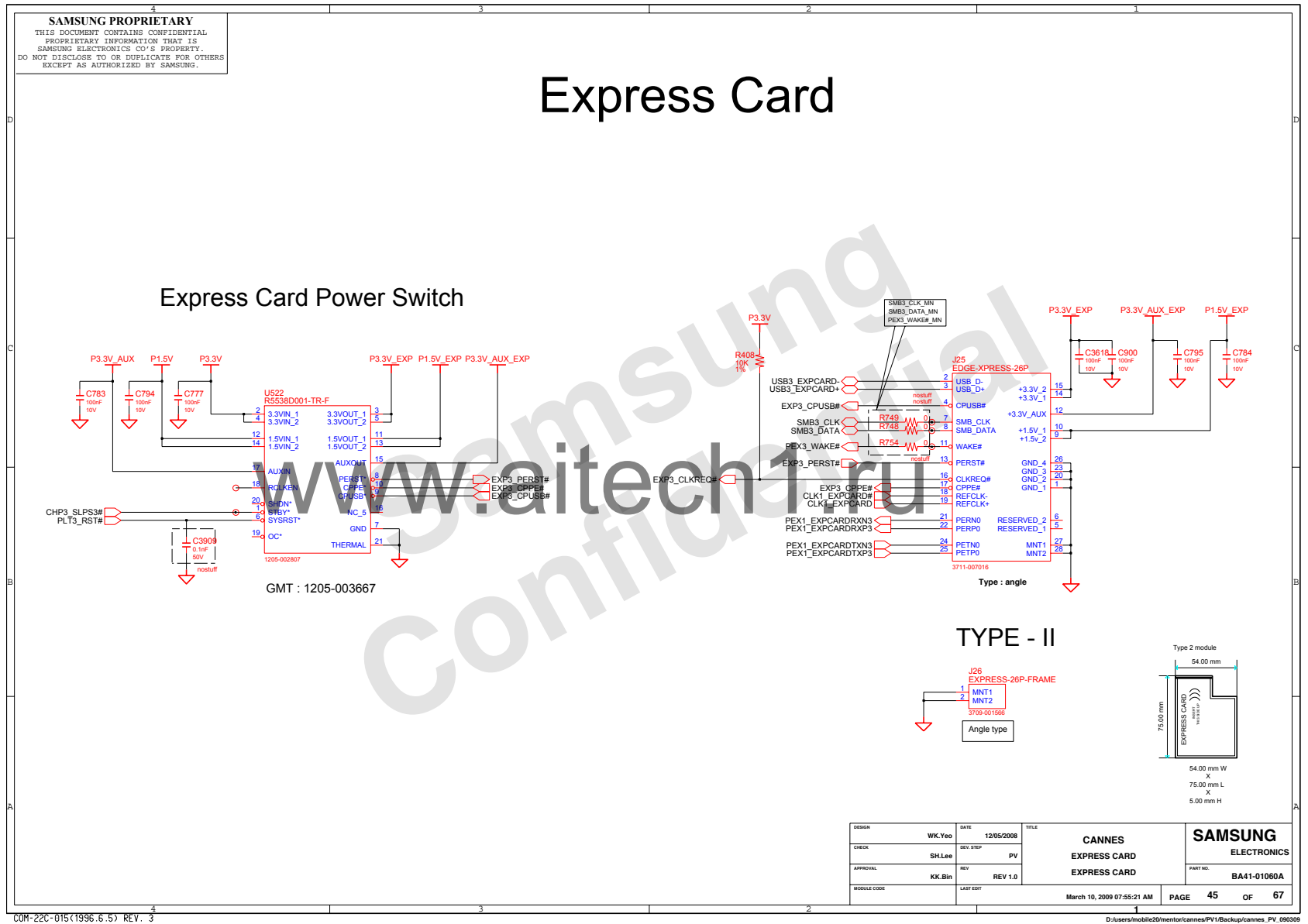
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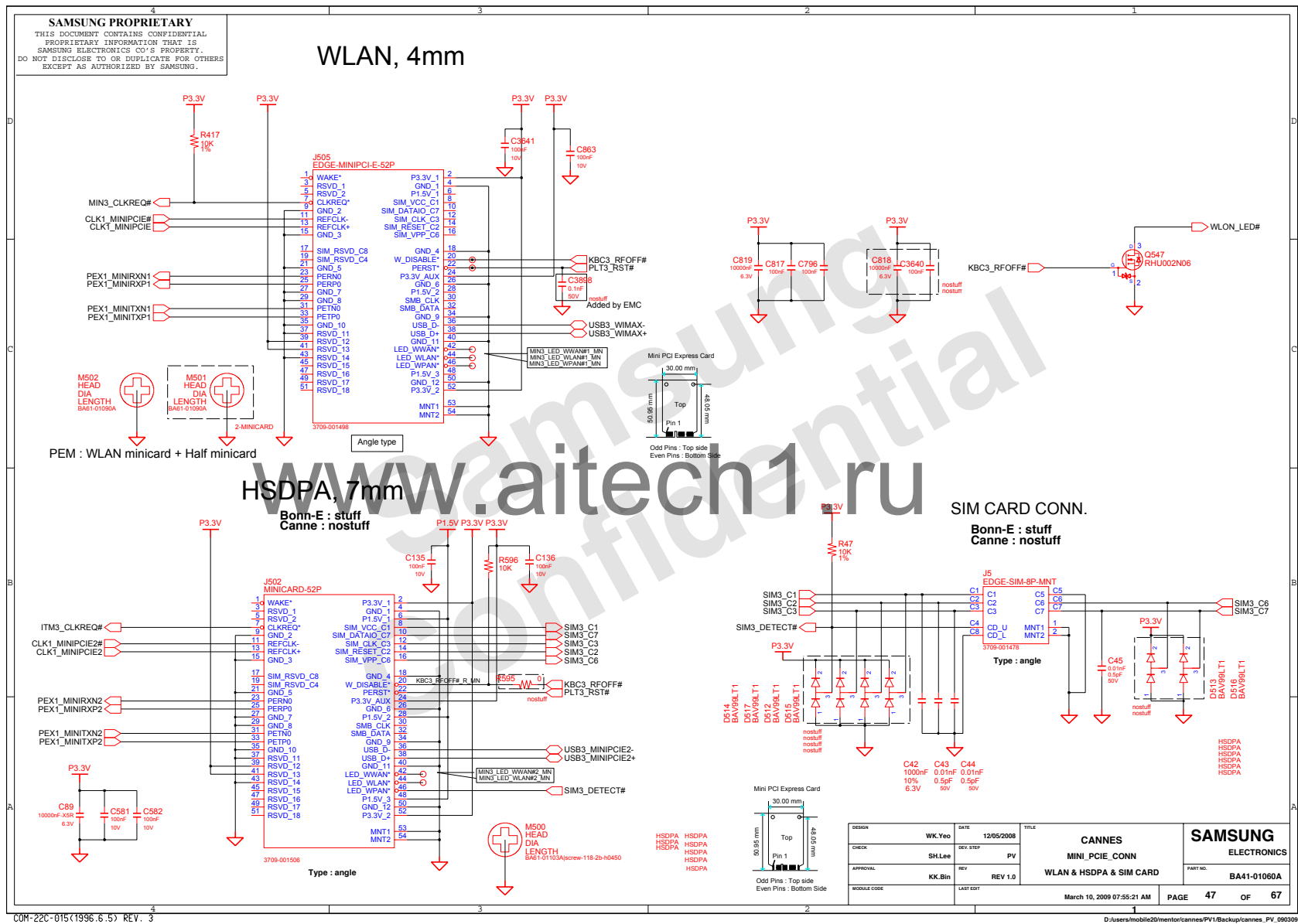


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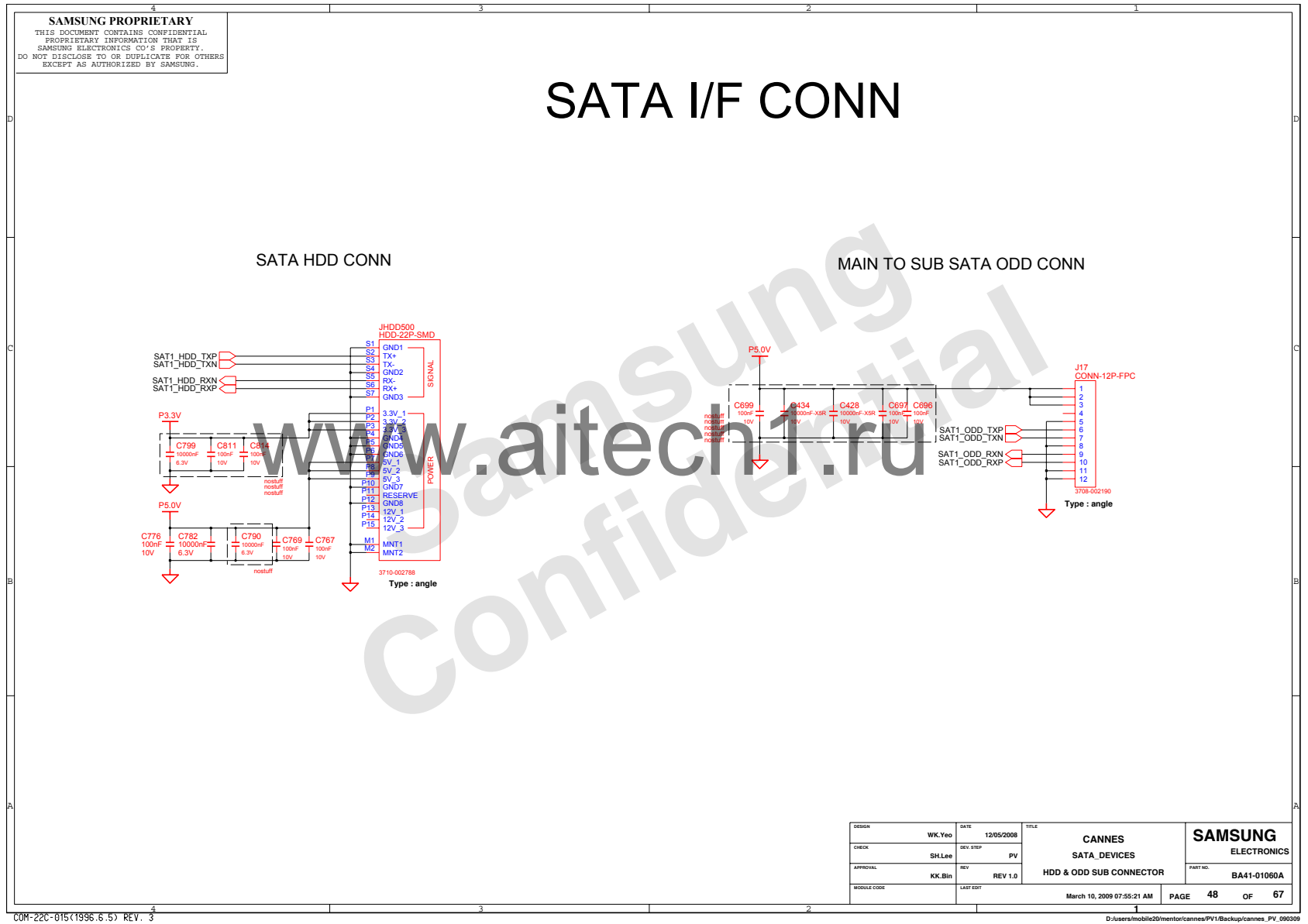




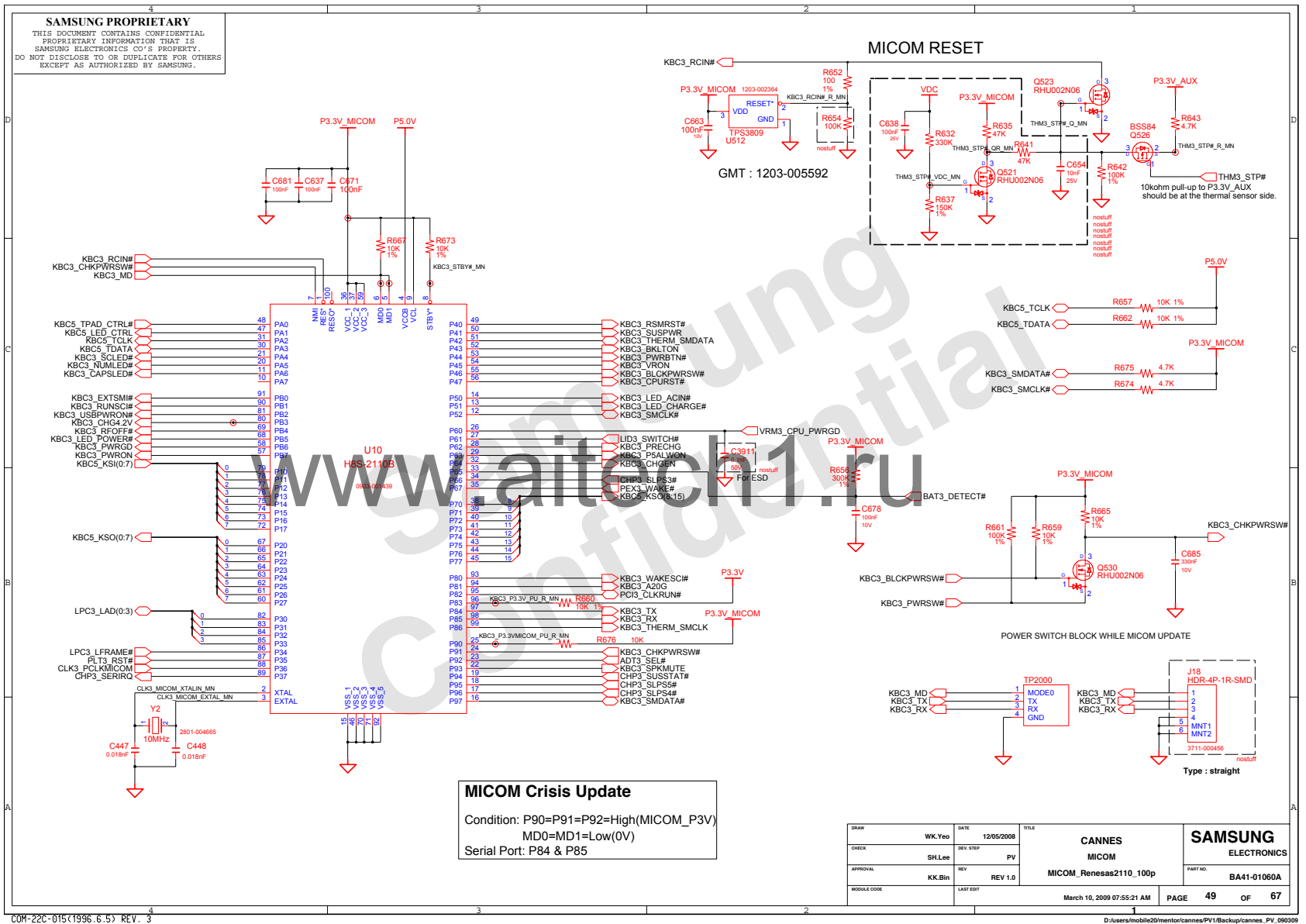
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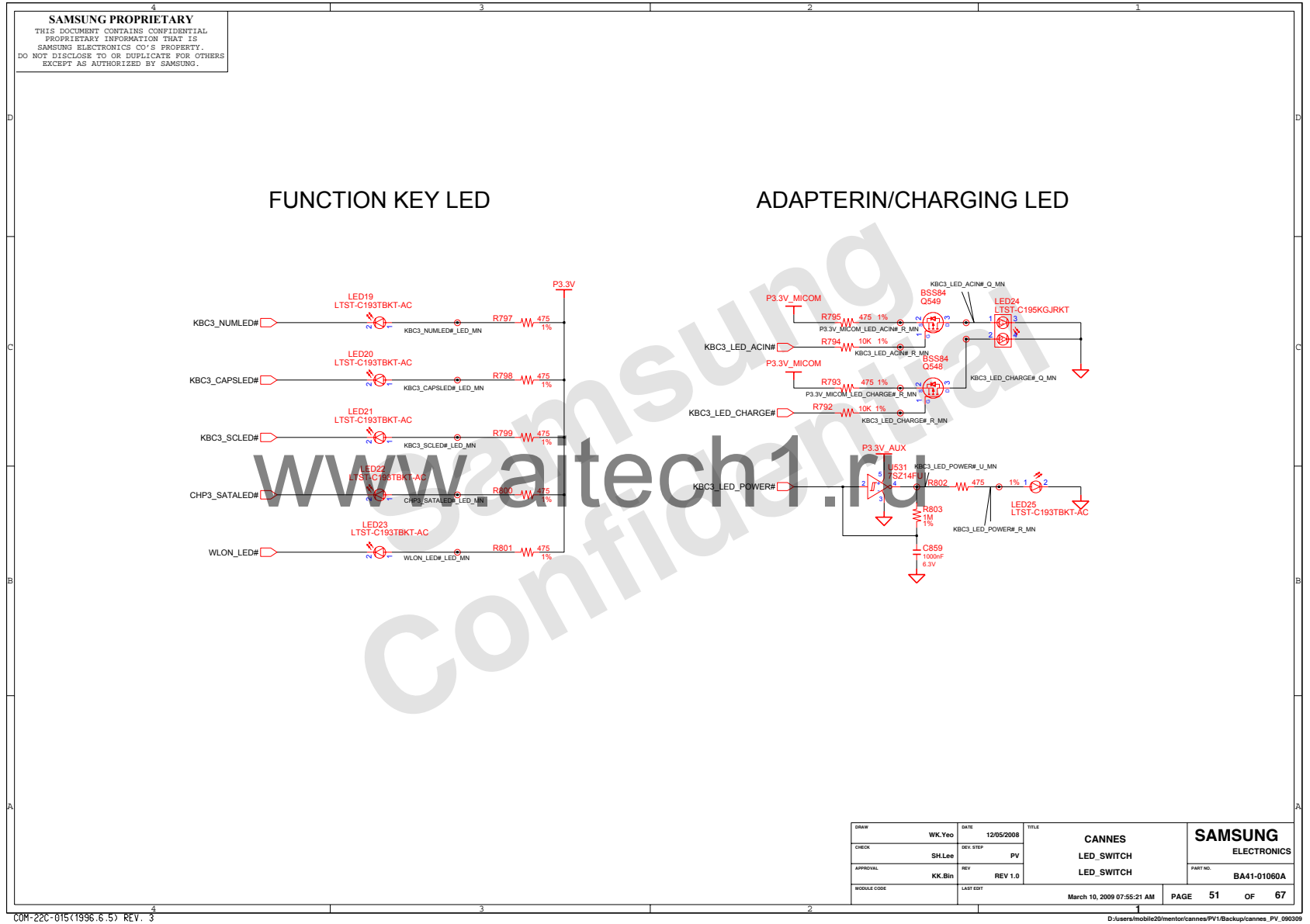


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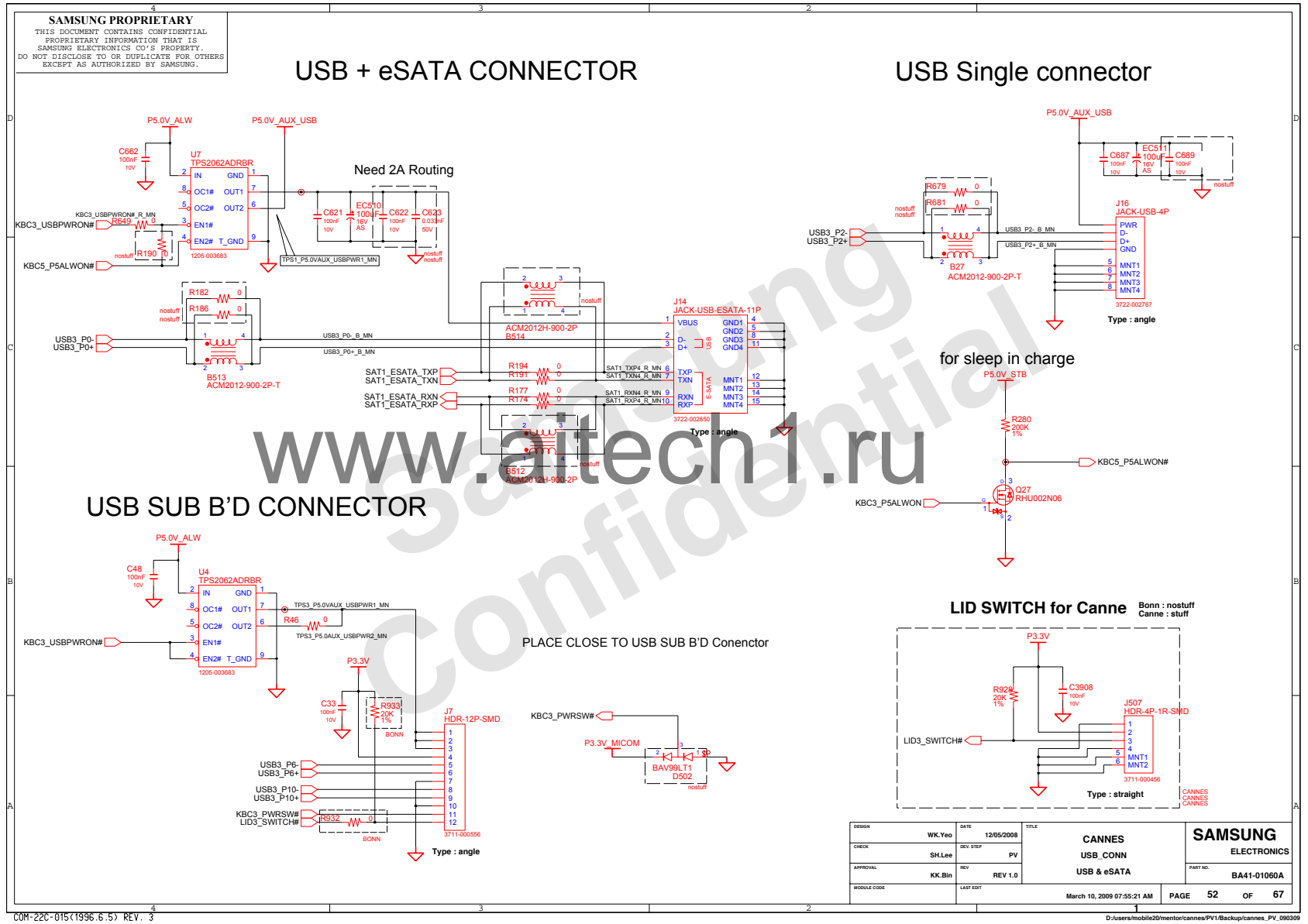




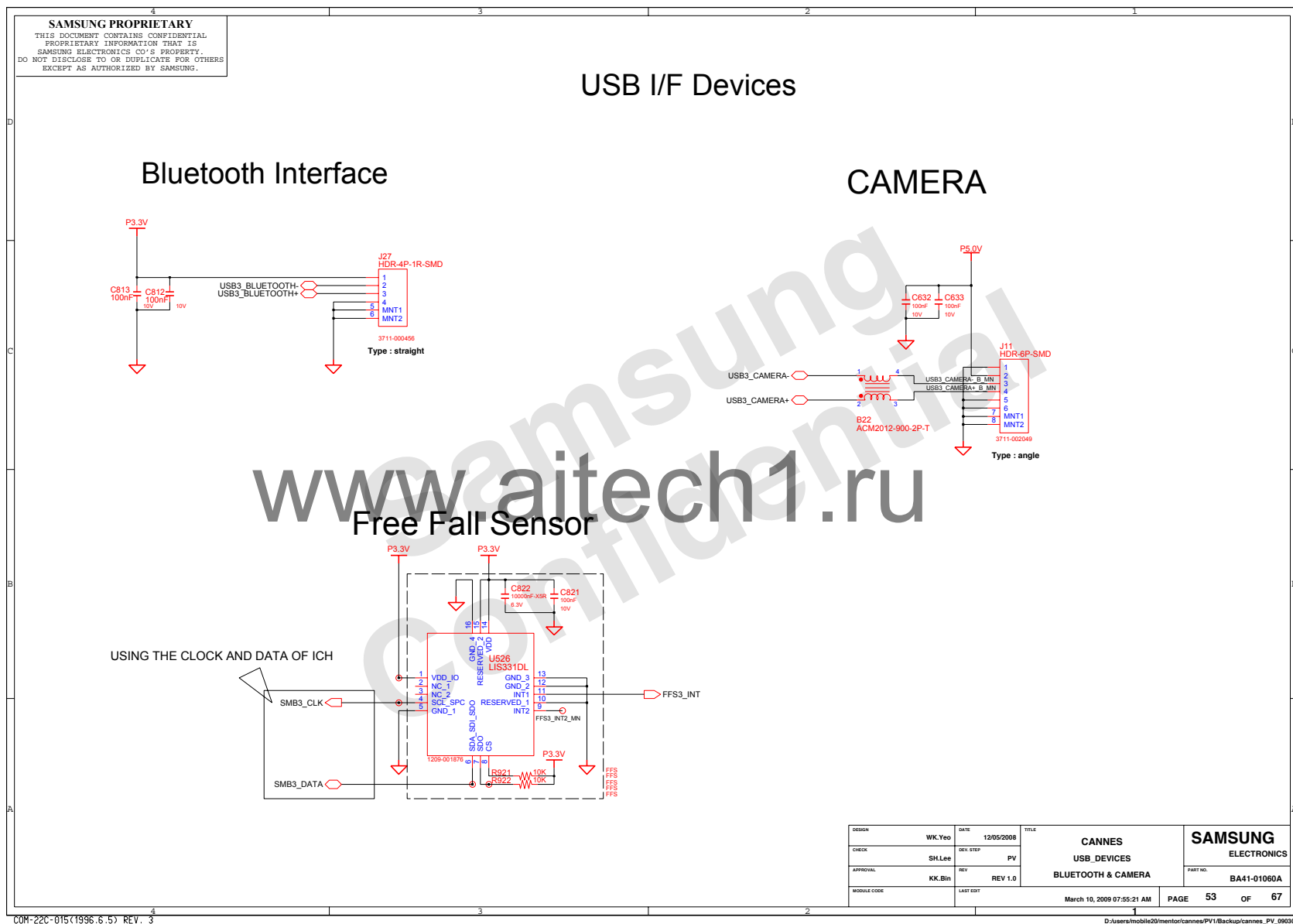
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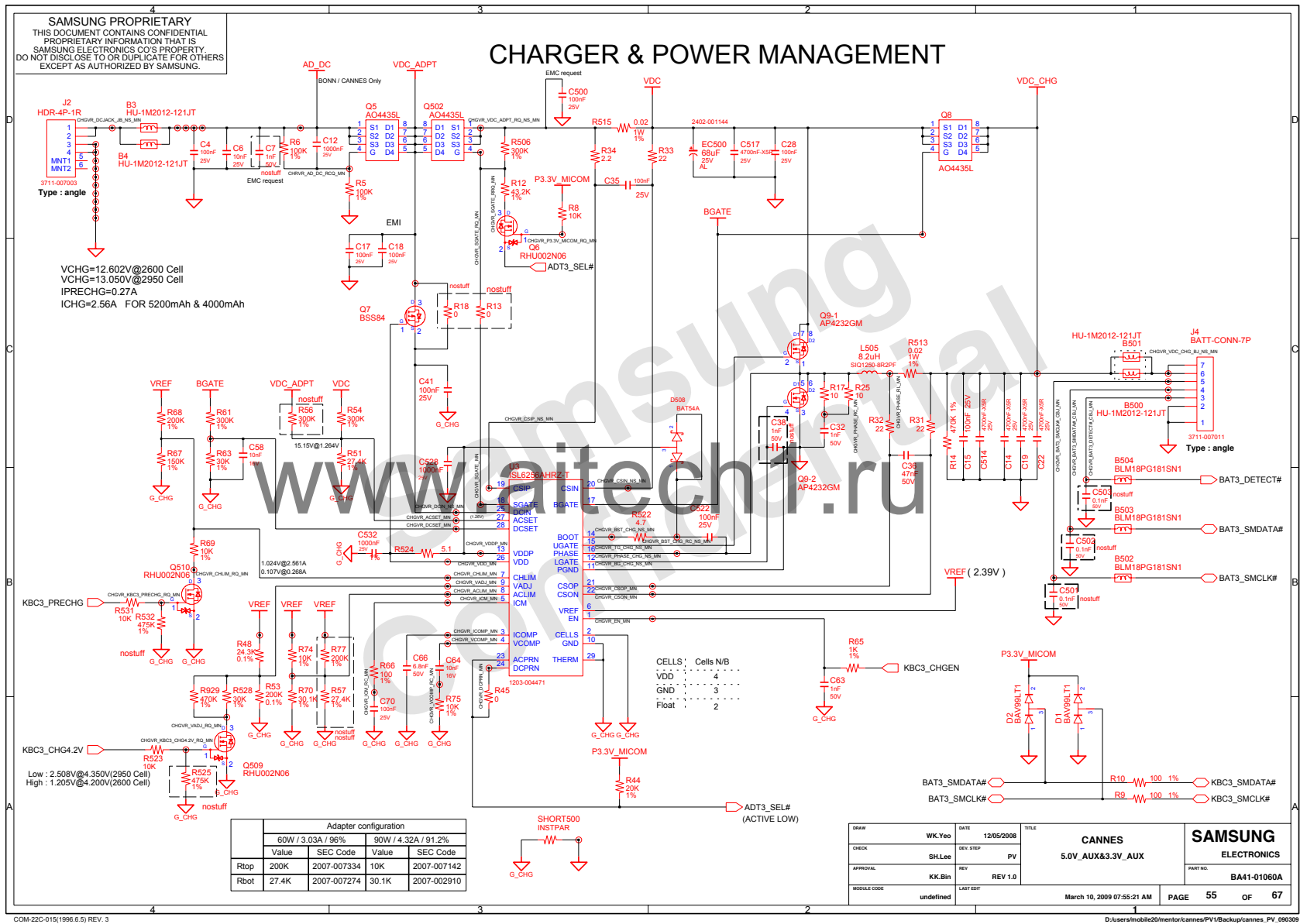


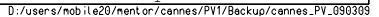
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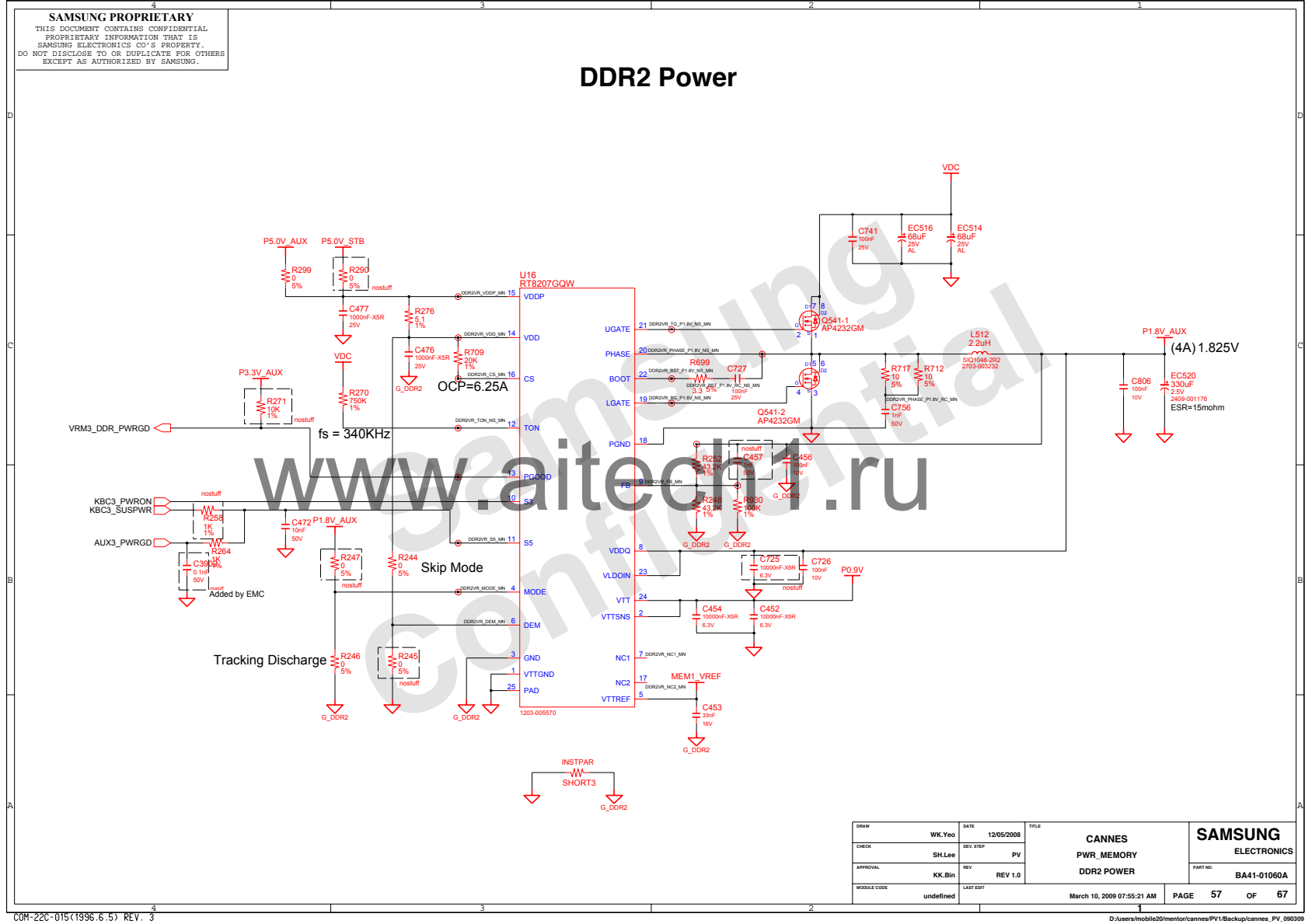


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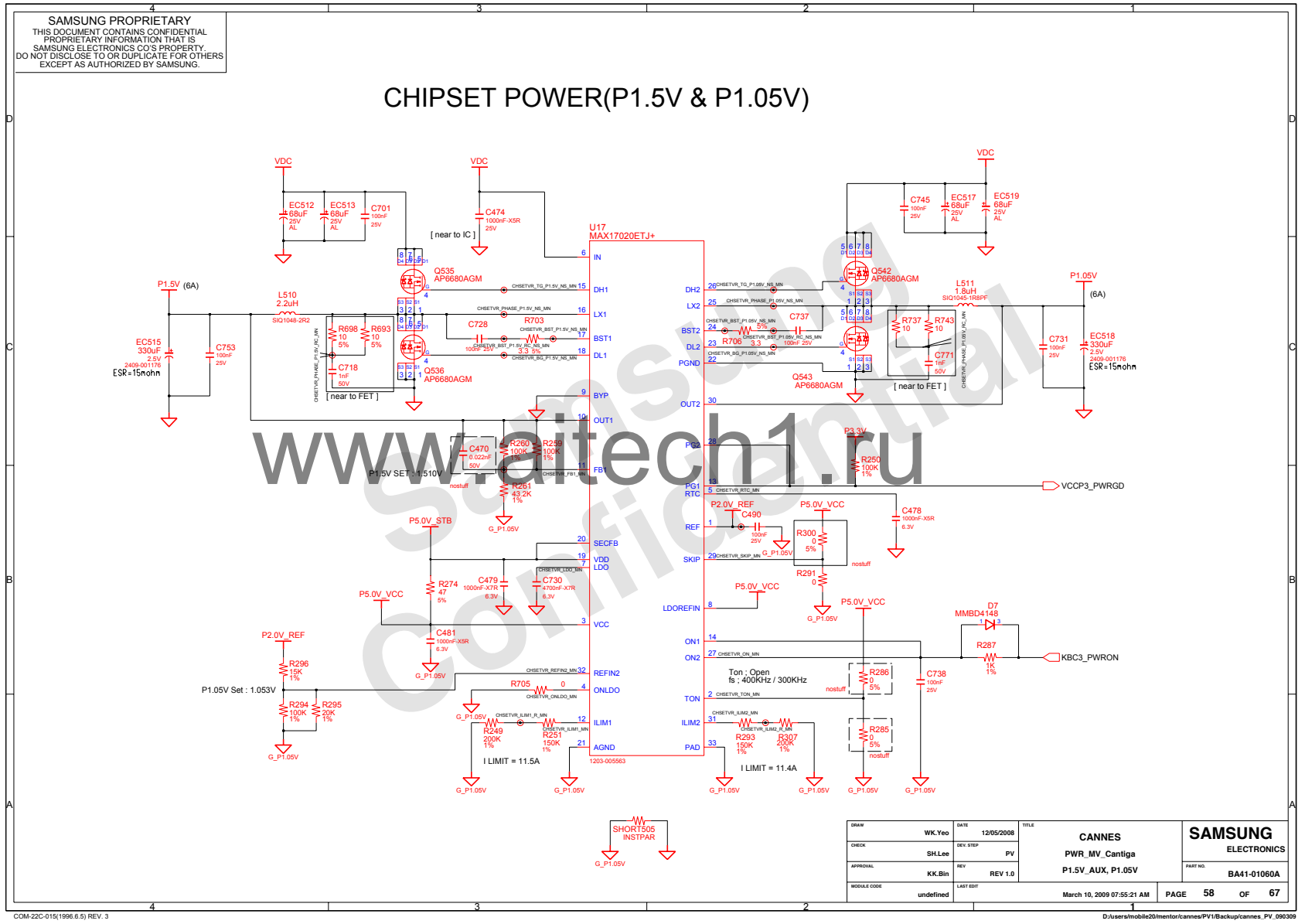




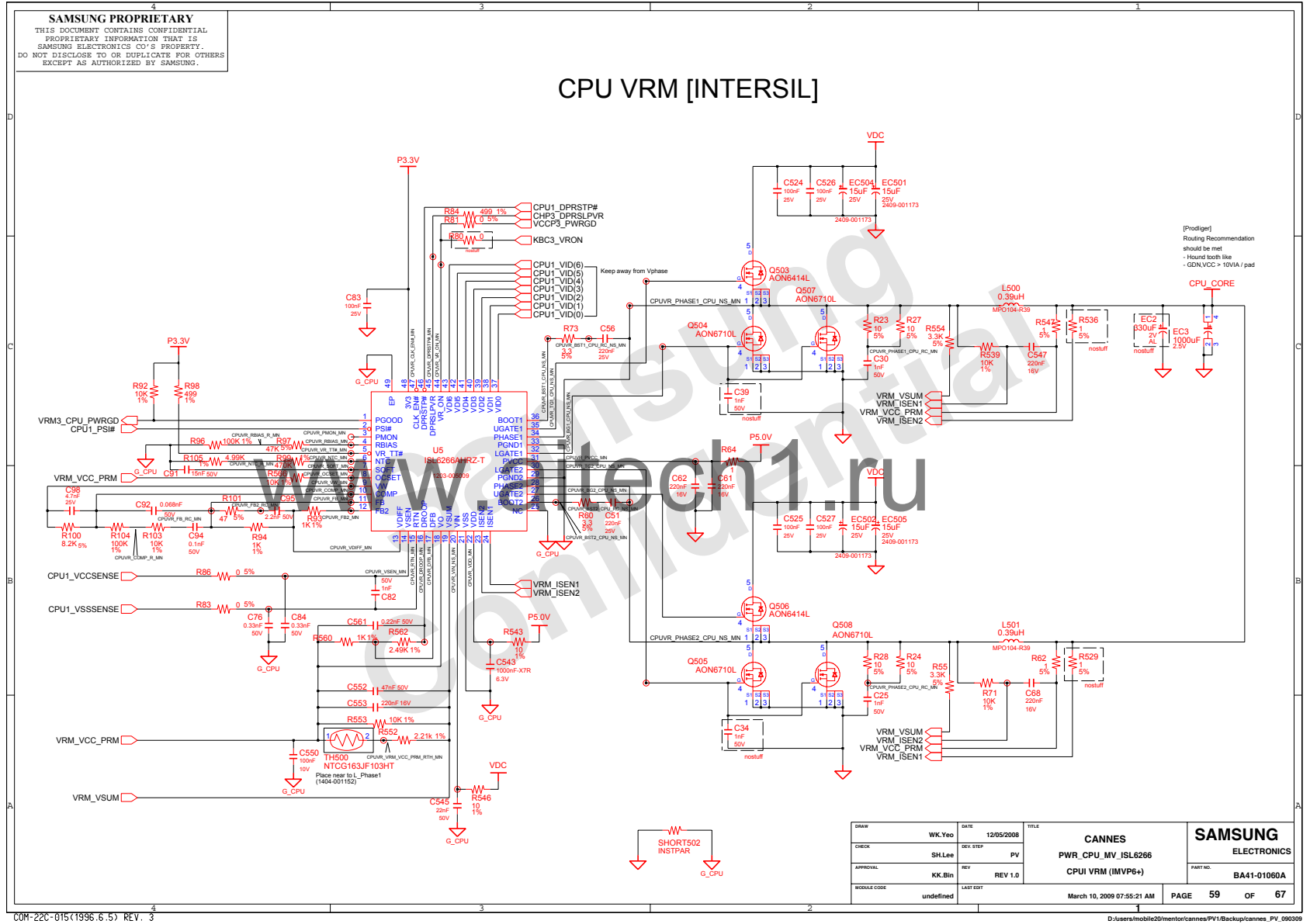
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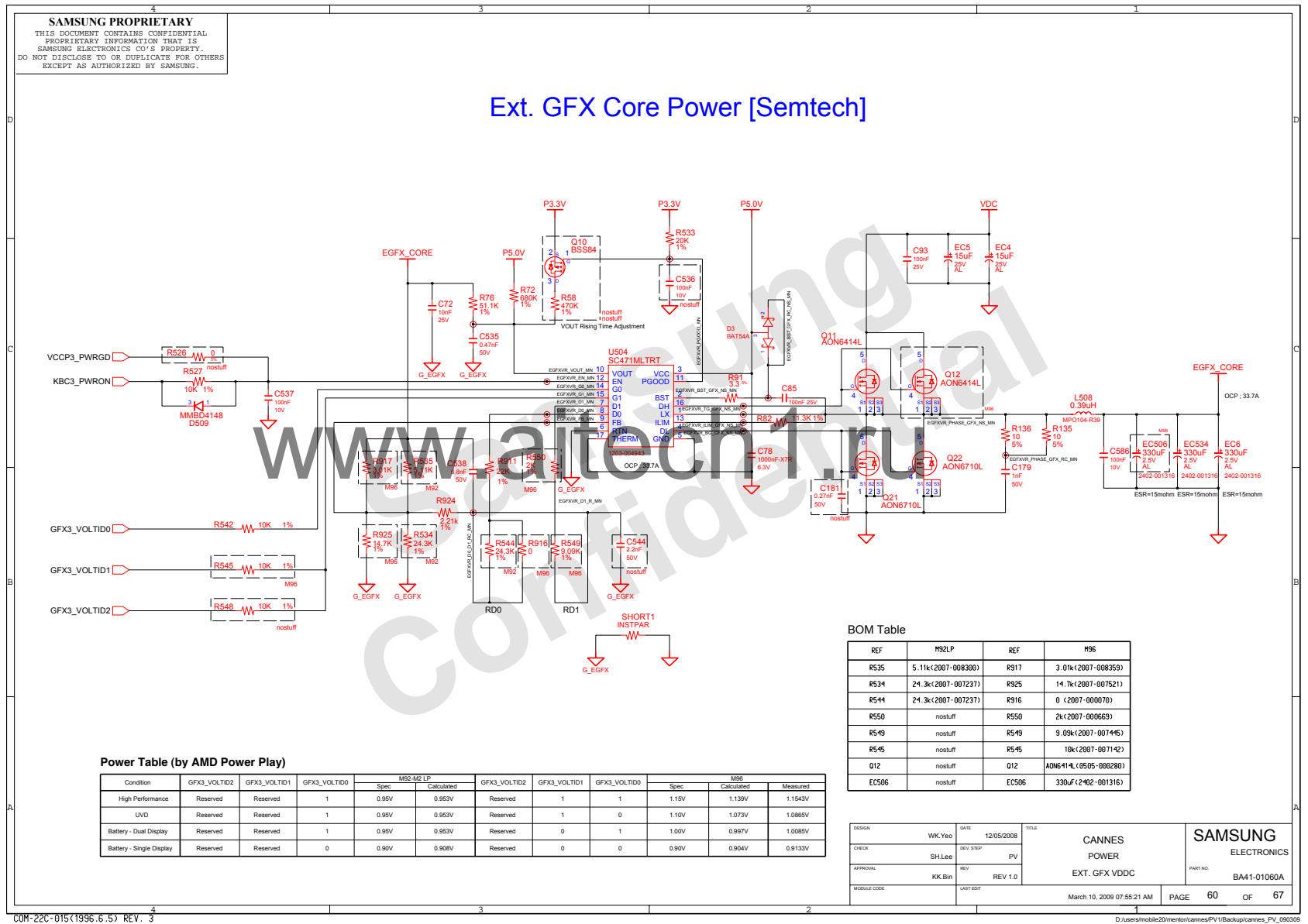
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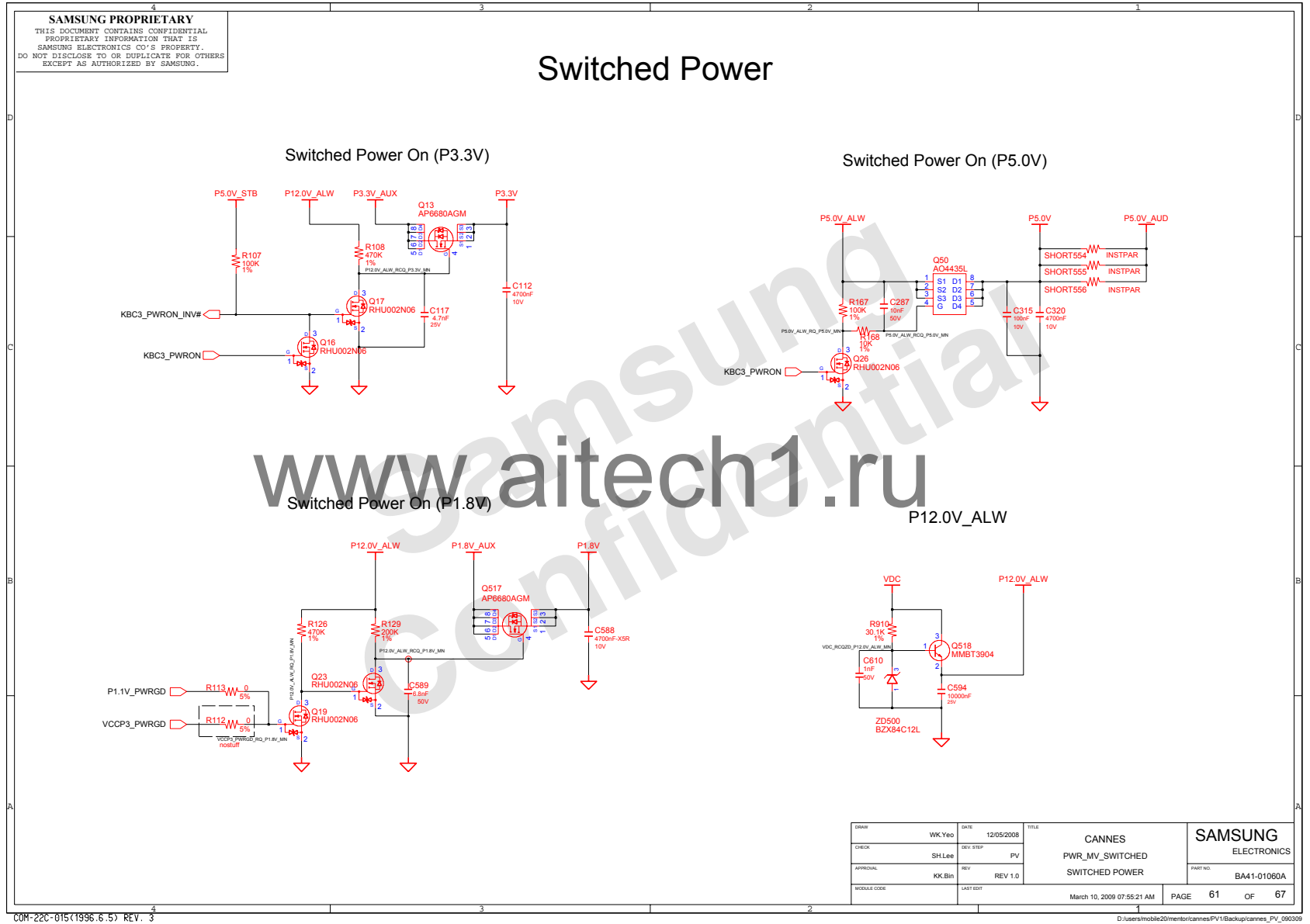
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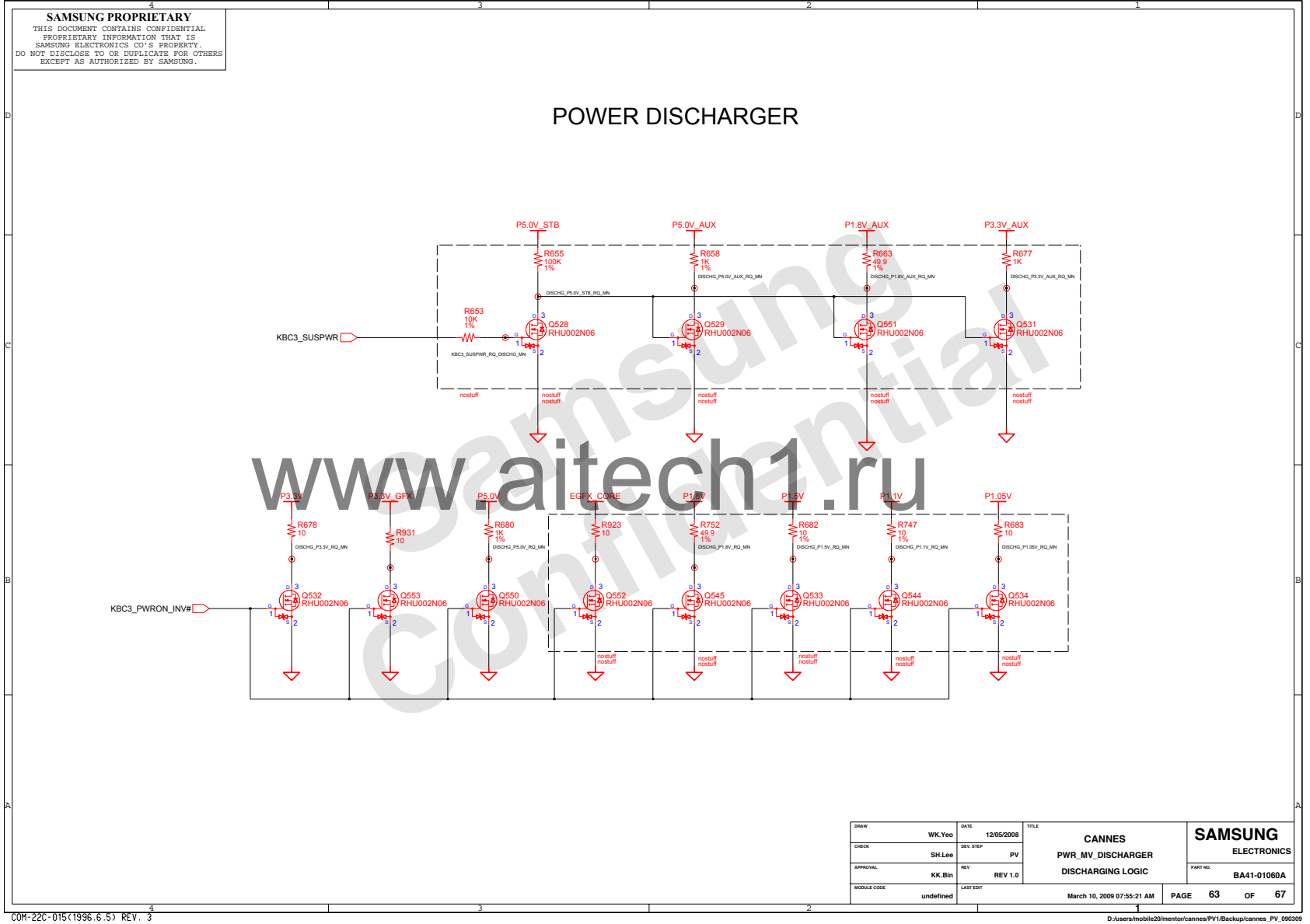


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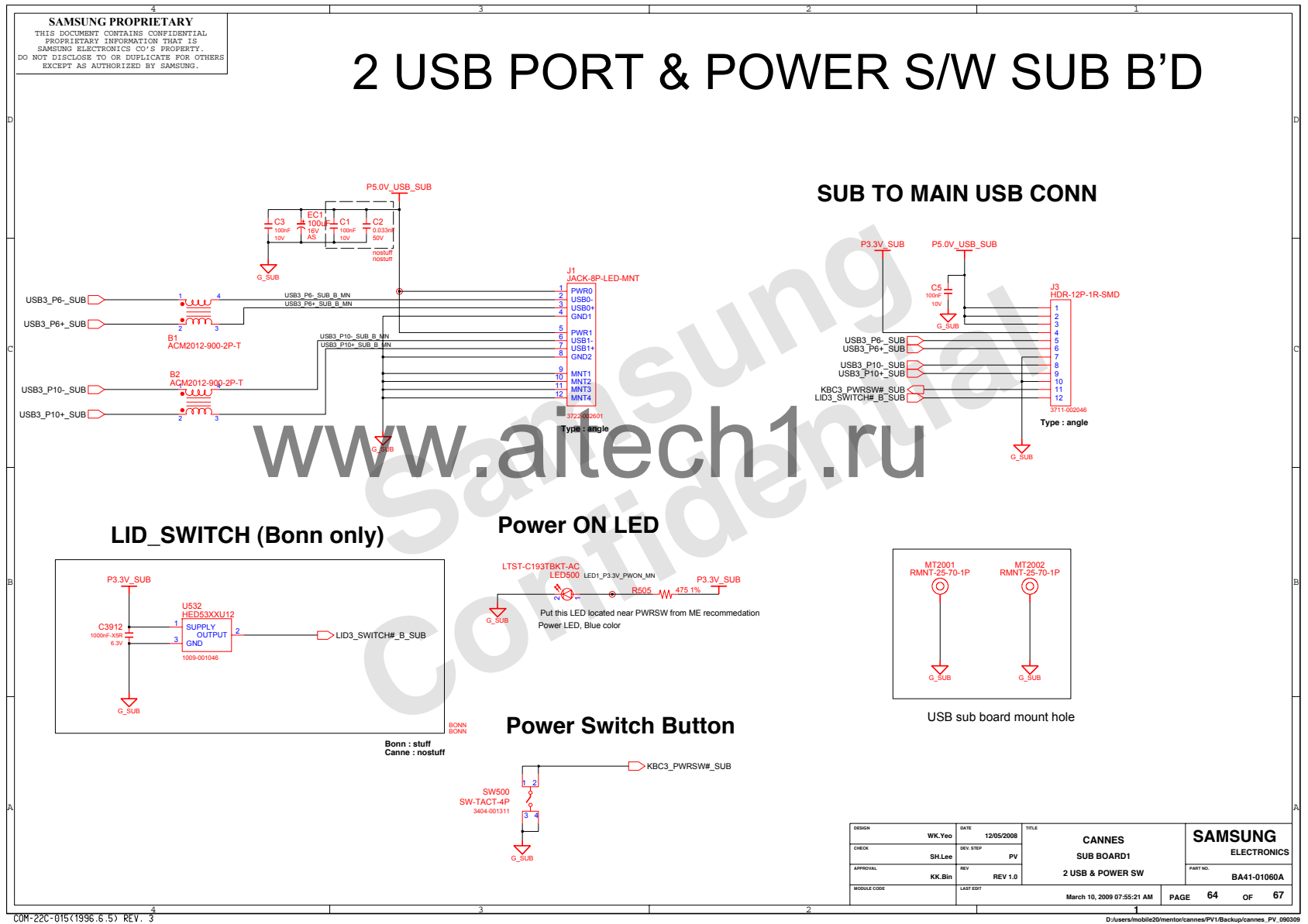




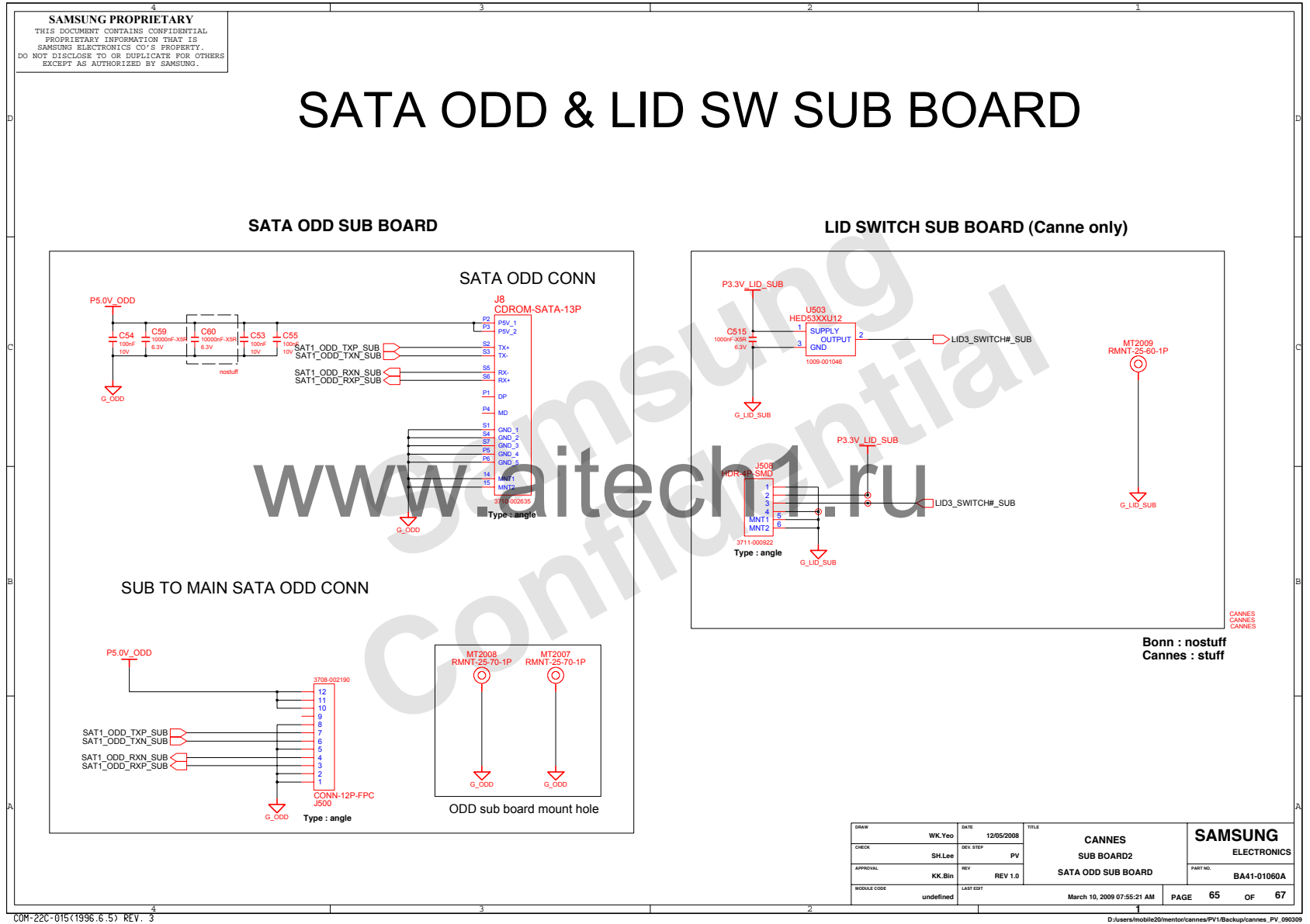
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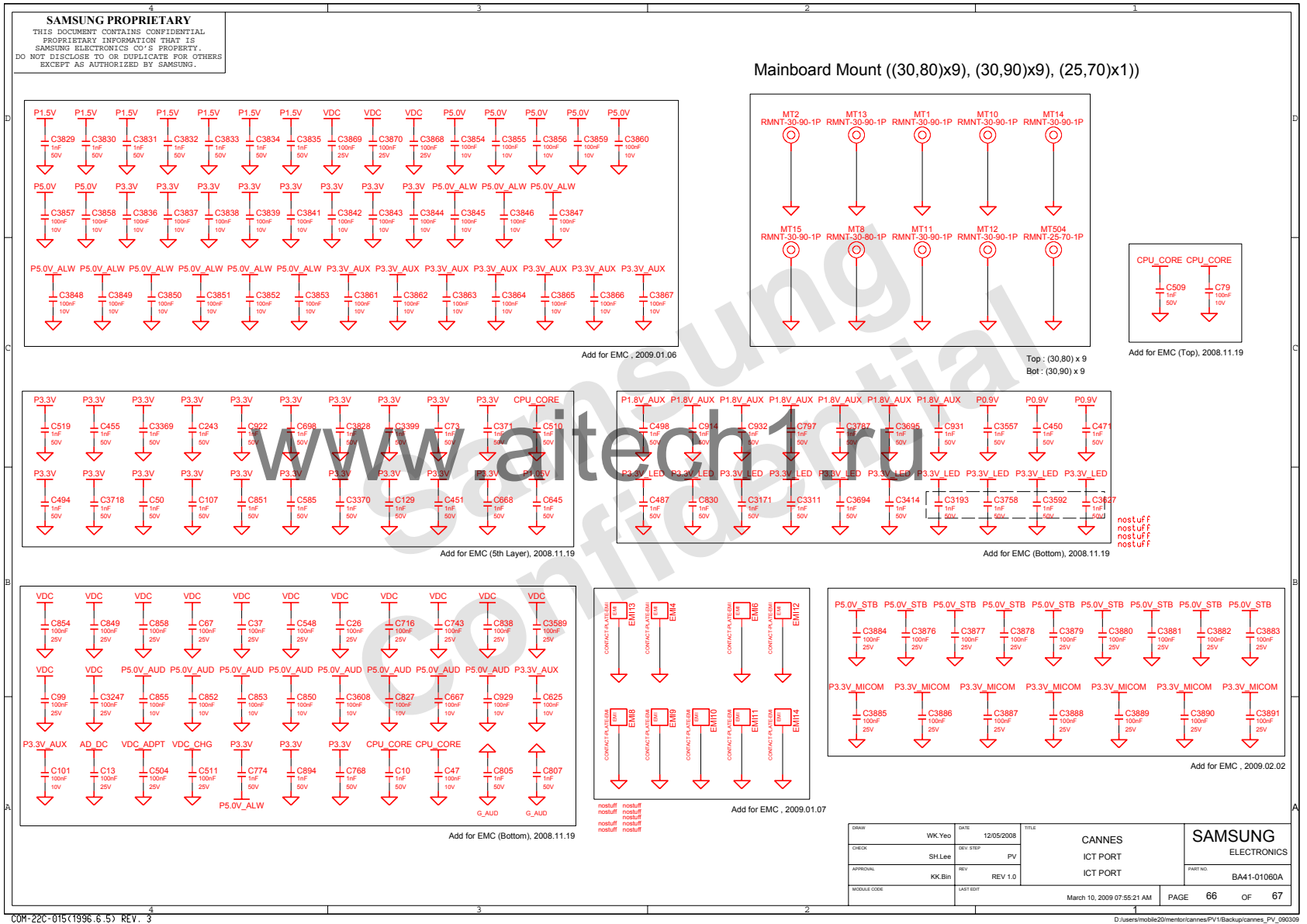
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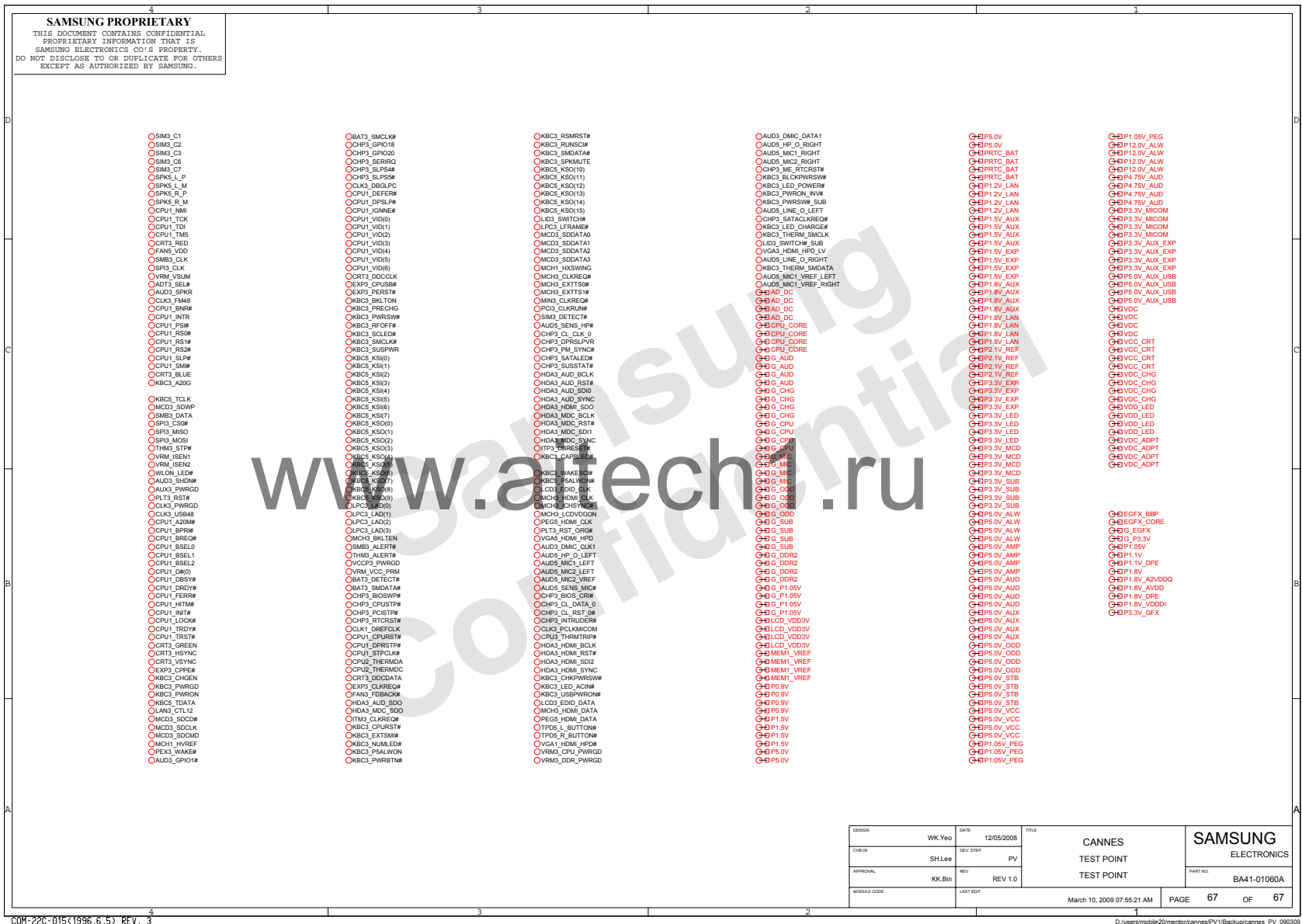
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